

**DSM TP 2017** 

8<sup>th</sup> International Summer School on Domain-Specific Modeling Theory and Practice

Montreal, Canada 10-14 July 2017

# Variability / Product Families

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Currently: The MathWorks, Inc. Previously: GSD Lab, University of Waterloo

# Acknowledgments

slides based on tutorials by

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Michał Antkiewicz, University of Waterloo

Krzysztof Czarnecki, University of Waterloo





# **Software-intensive Products Come in Many Variants**

3





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# **Domain Engineering**

### aka Product Line Engineering aka Product Family Engineering

#### **Application Engineering**

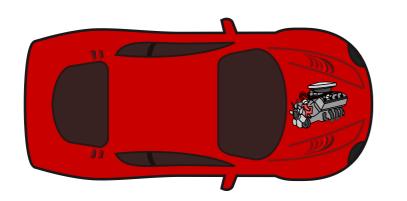
Development with Reuse

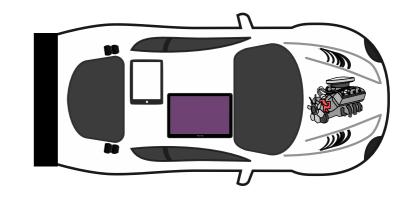
#### Single Product

#### **Domain Engineering**

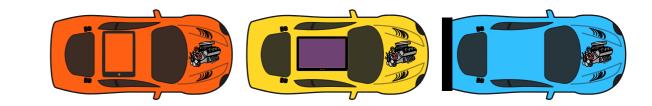
Development for Reuse

**Product Family** 







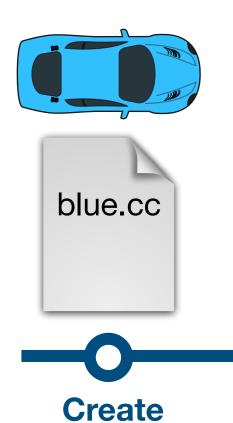


# Why Domain Engineering?

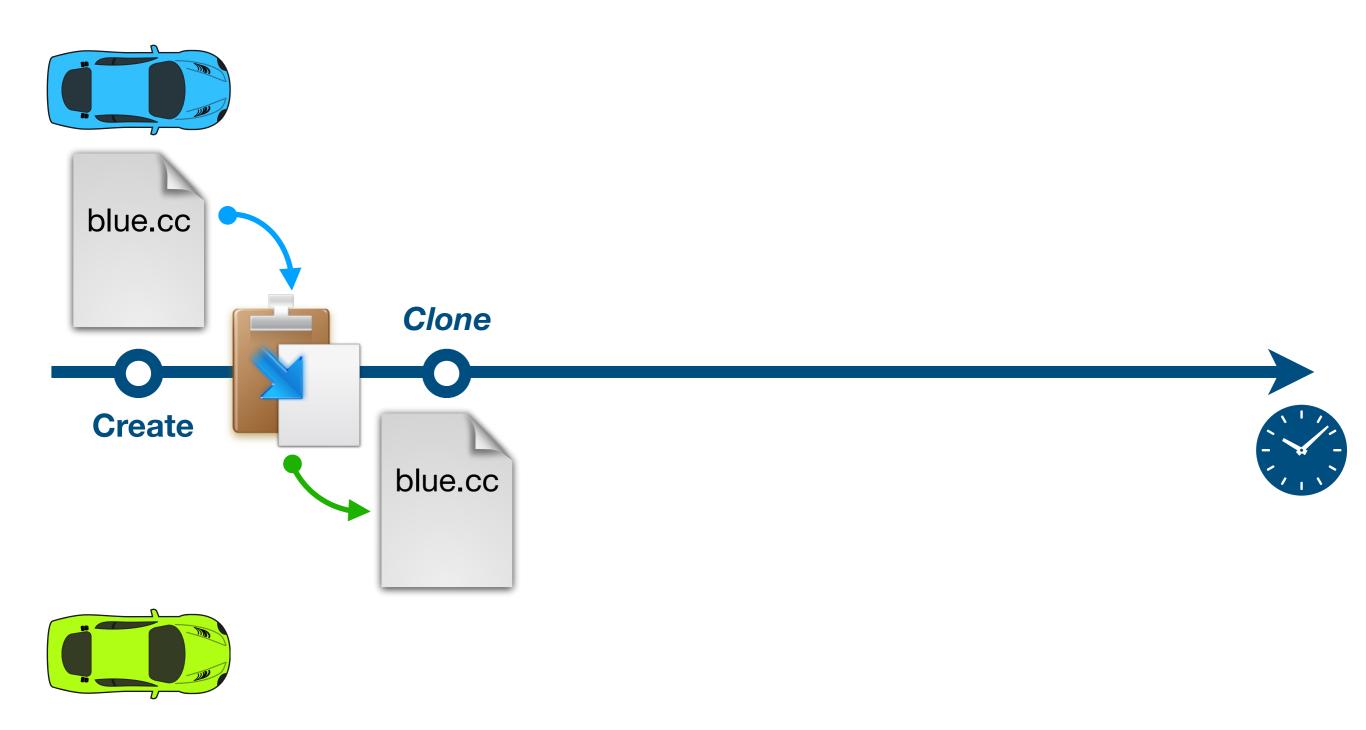
# Why Domain Engineering?

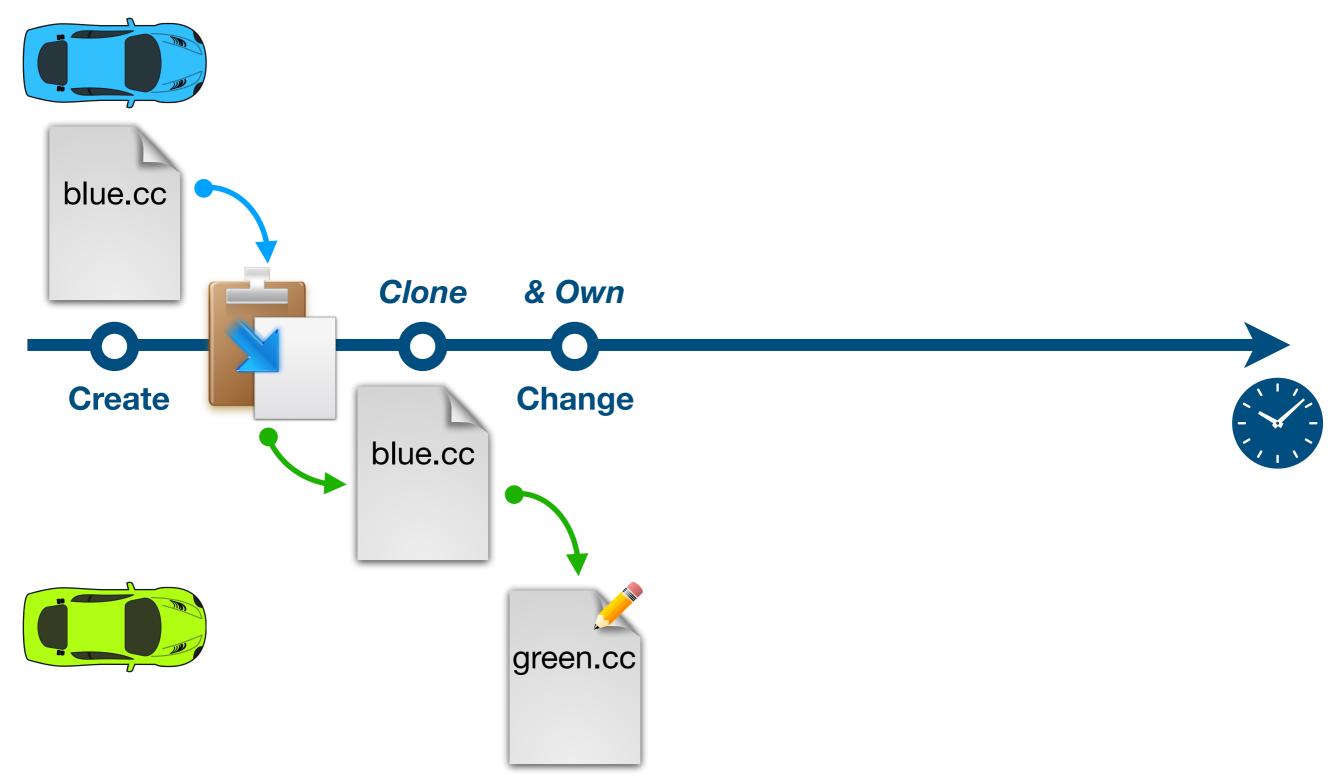
#### ...because

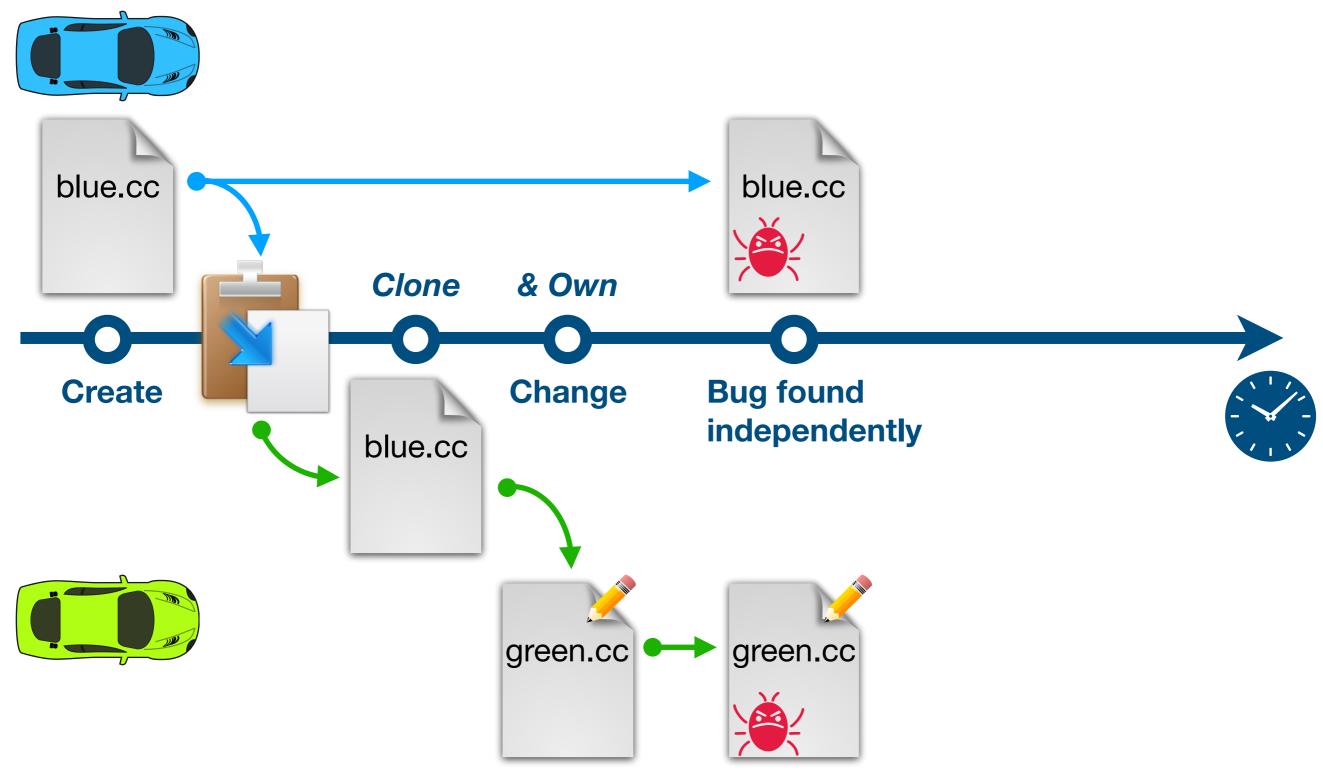
opportunistic reuse does not scale

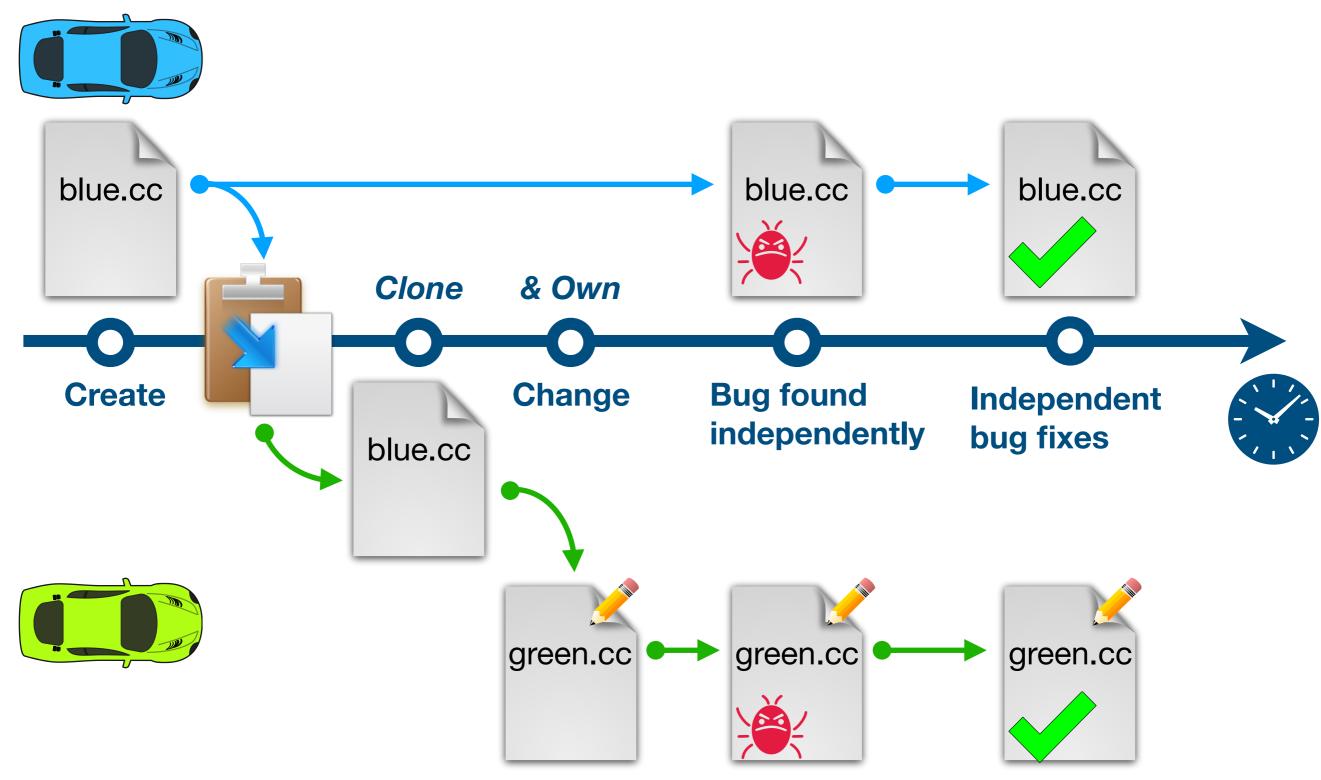


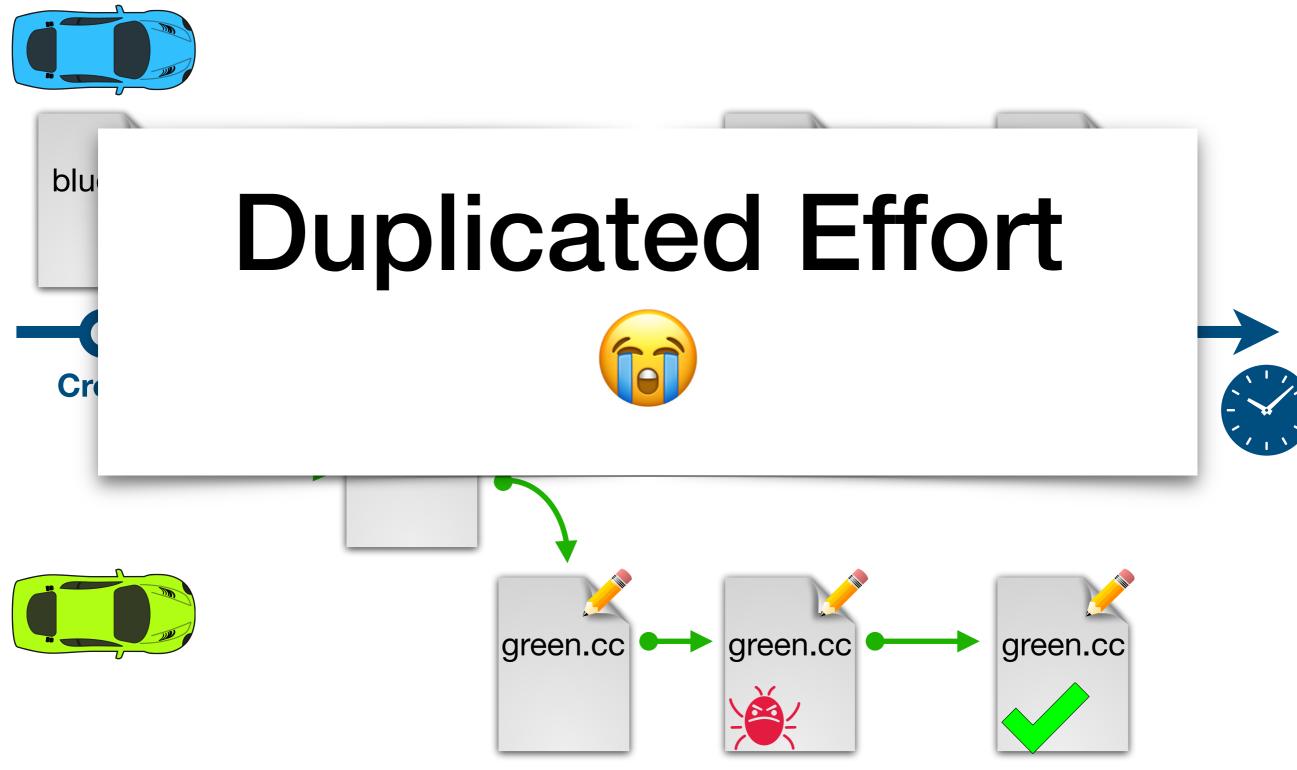










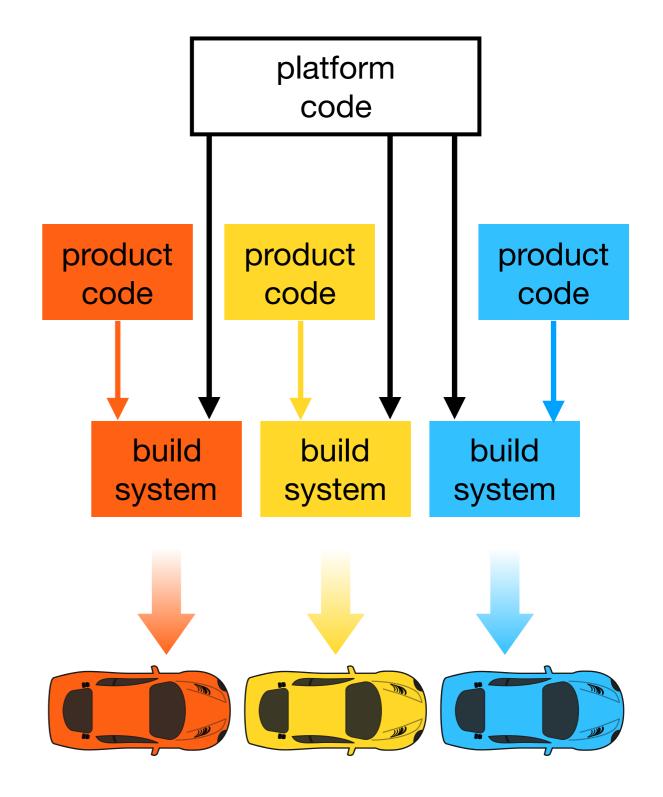


# **Cloning as Opportunistic Reuse**

Dubinsky et al., Exploratory Study of Cloning in Industrial SPLs, CSMR 2013

- + Easy, no special tooling required
- + Quickly available functionality

- **No** sharing (fixes & features)
- Maintain yourself (test, debug, change)
- Product specific code grows
- Platform code diminishes and degrades



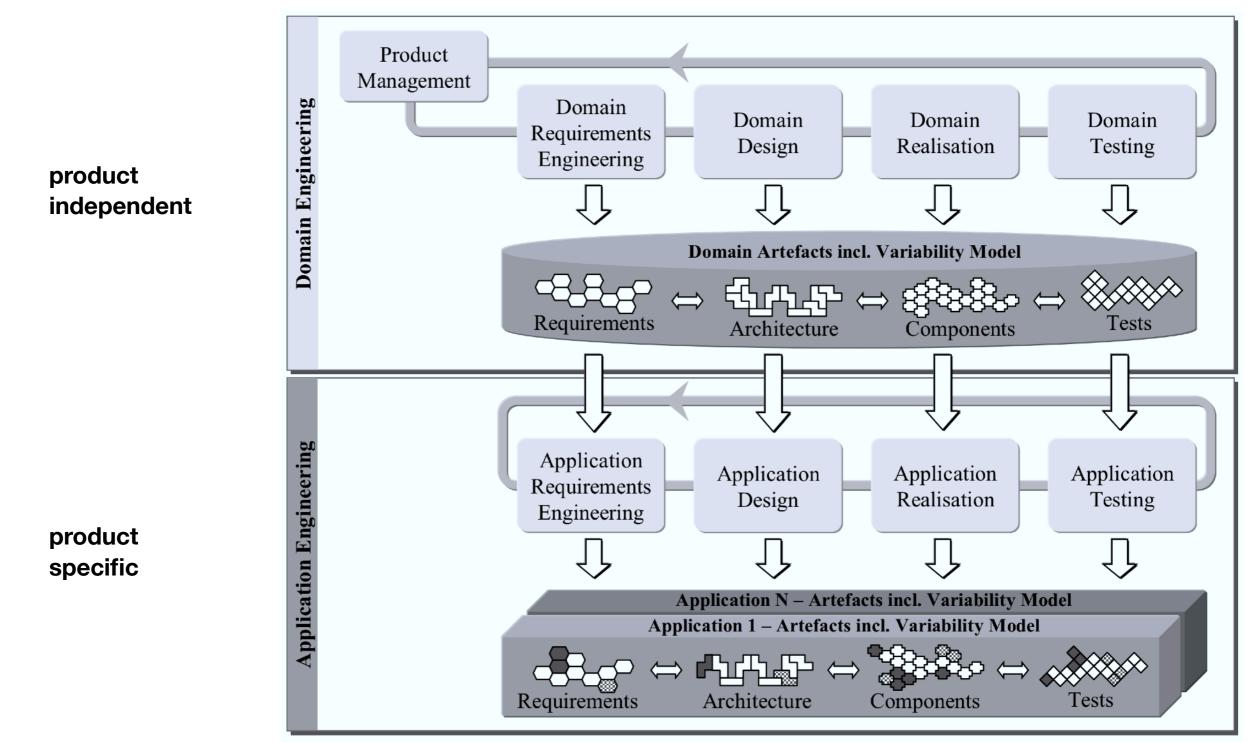
# Successful Reuse

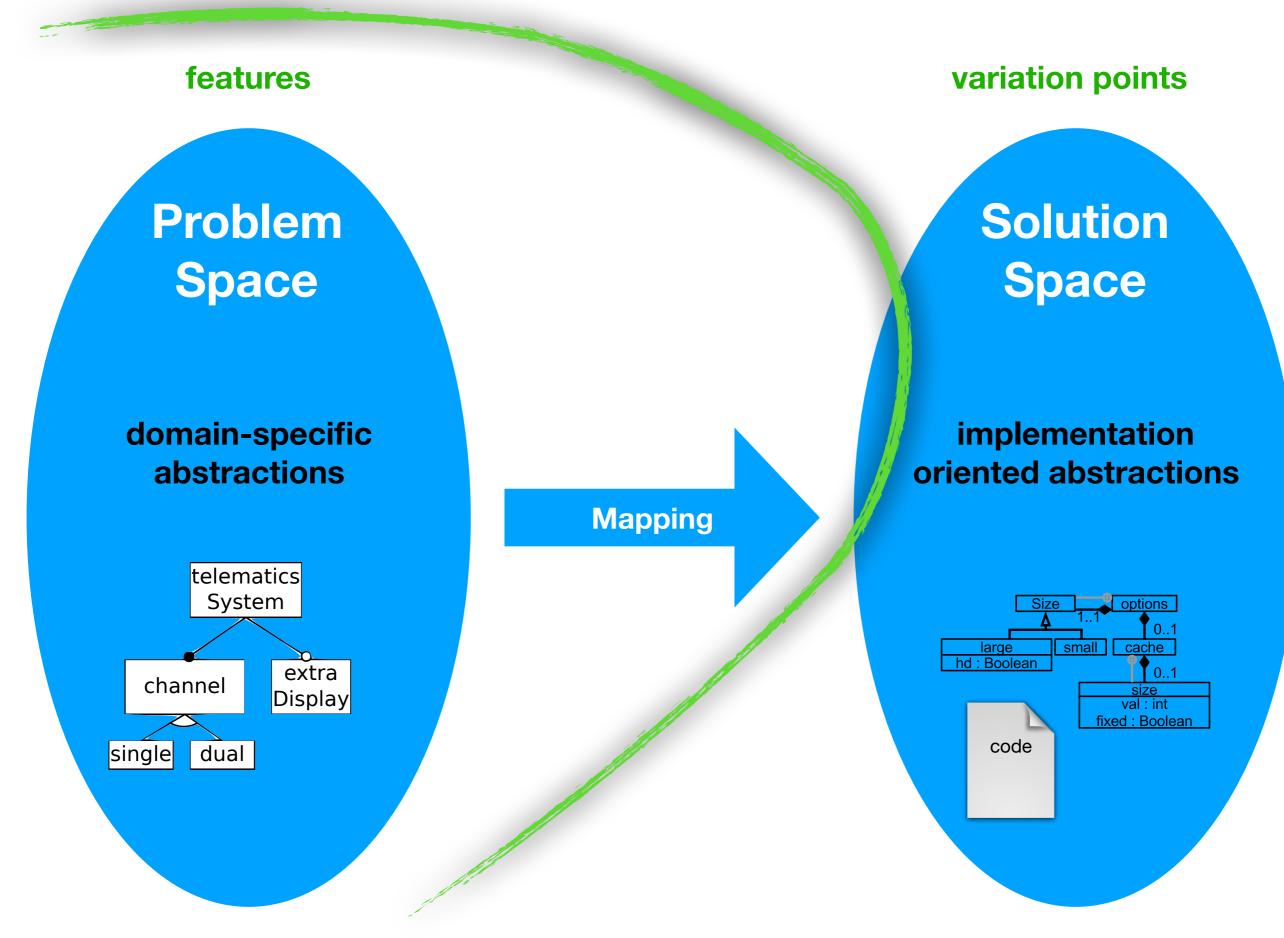
Proactive Planned

Managed

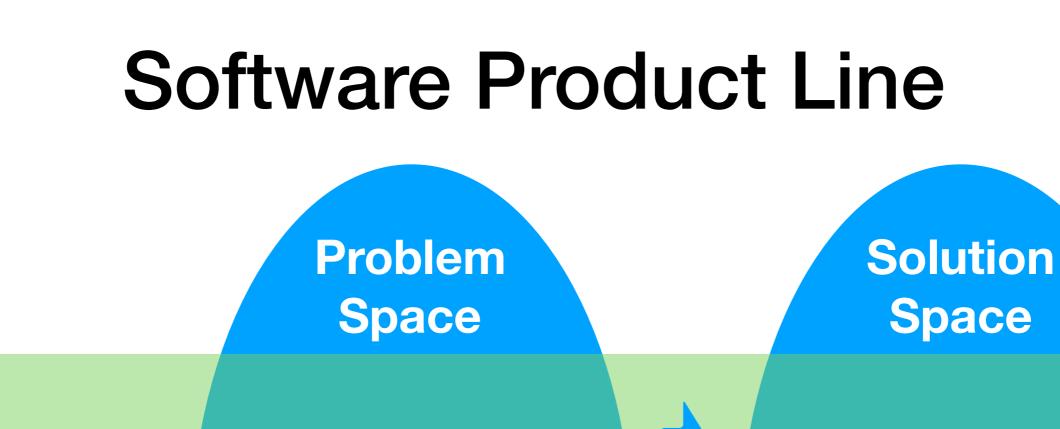
# **Domain vs Application Engineering**

Pohl et al., Software Product Line Engineering, Springer Science, 2005





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Domain Engineering

#### Variability Abstraction

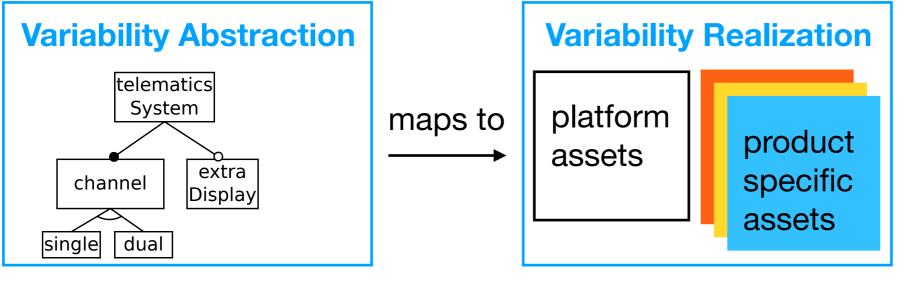
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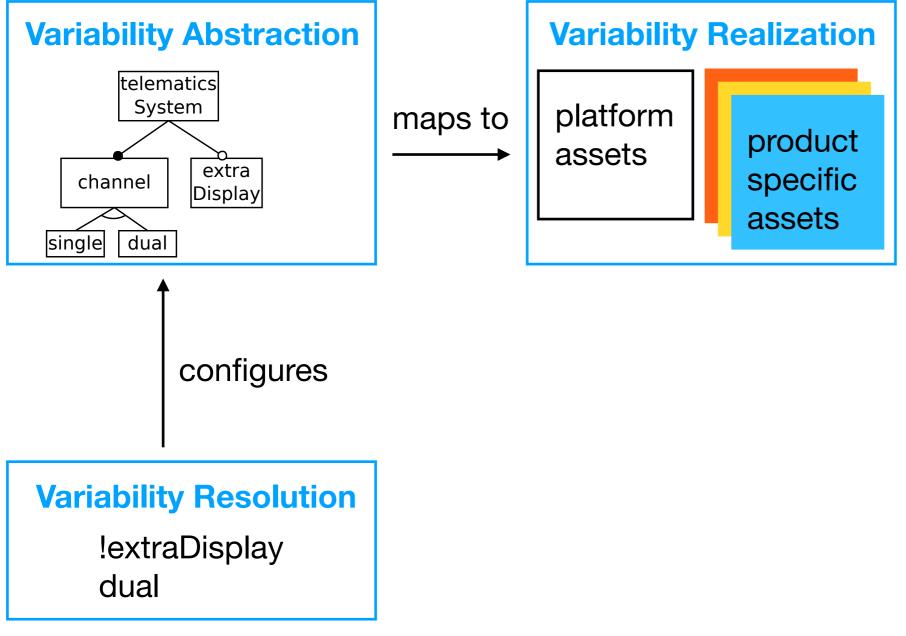
#### Variability Realization

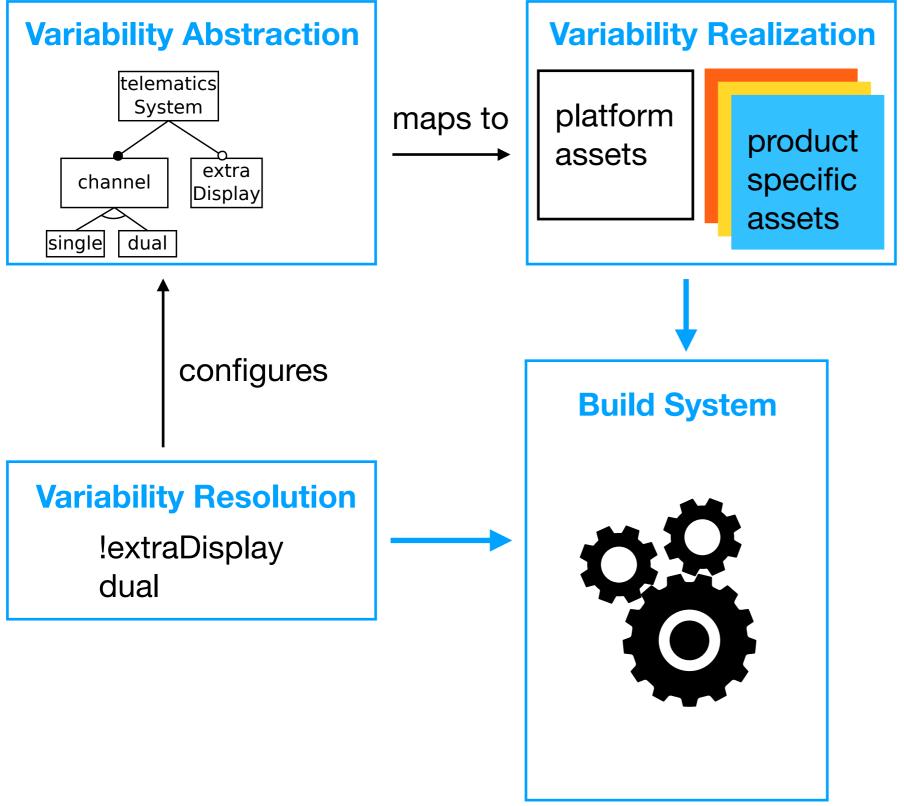
Application Engineering

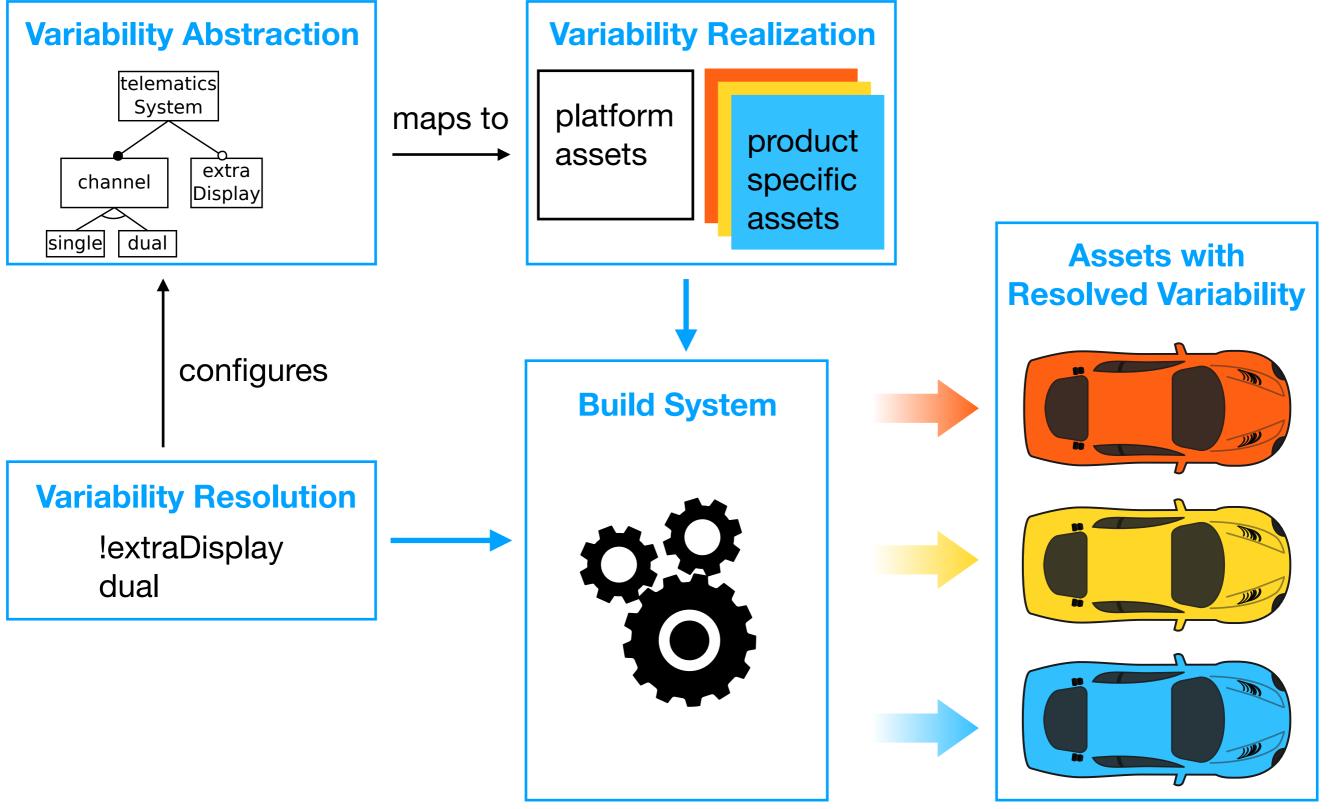
#### Variability Resolution

Assets with Resolved Variability



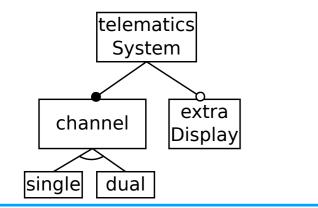


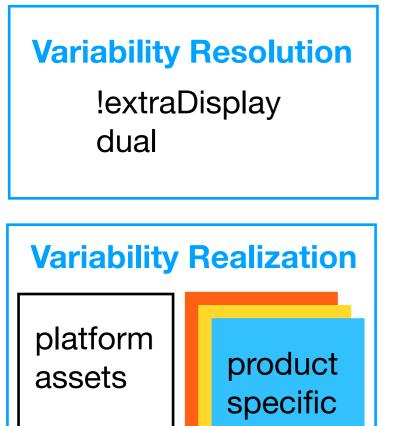




# **Implementation Technologies**

#### **Variability Abstraction**





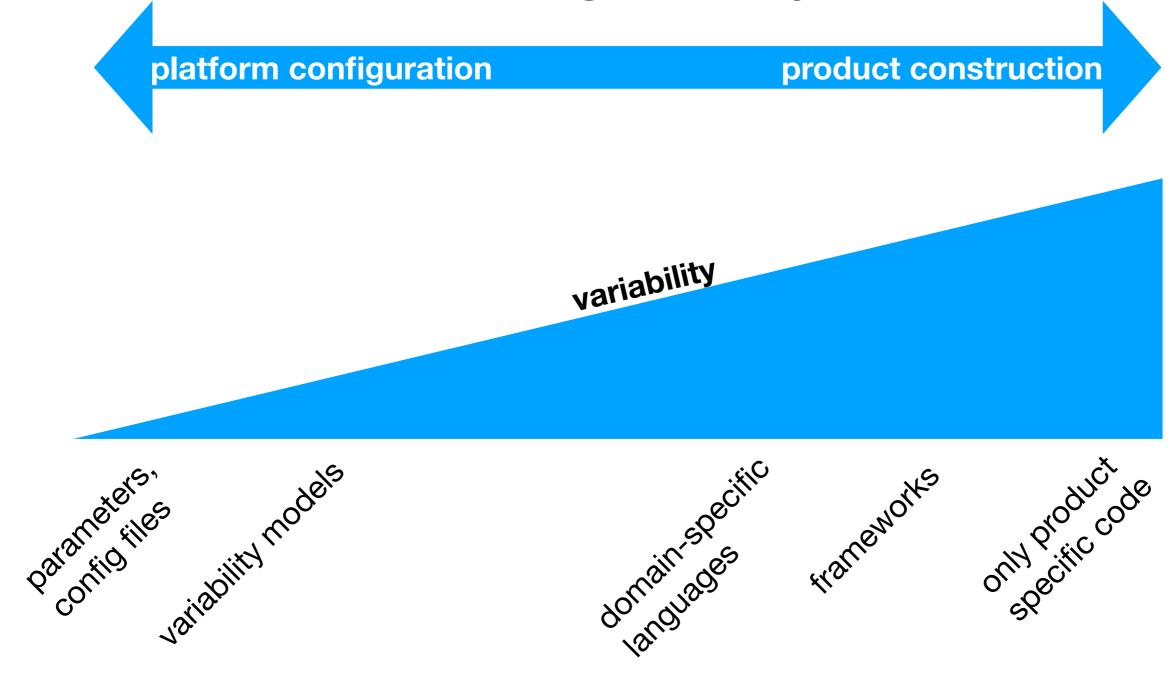
assets

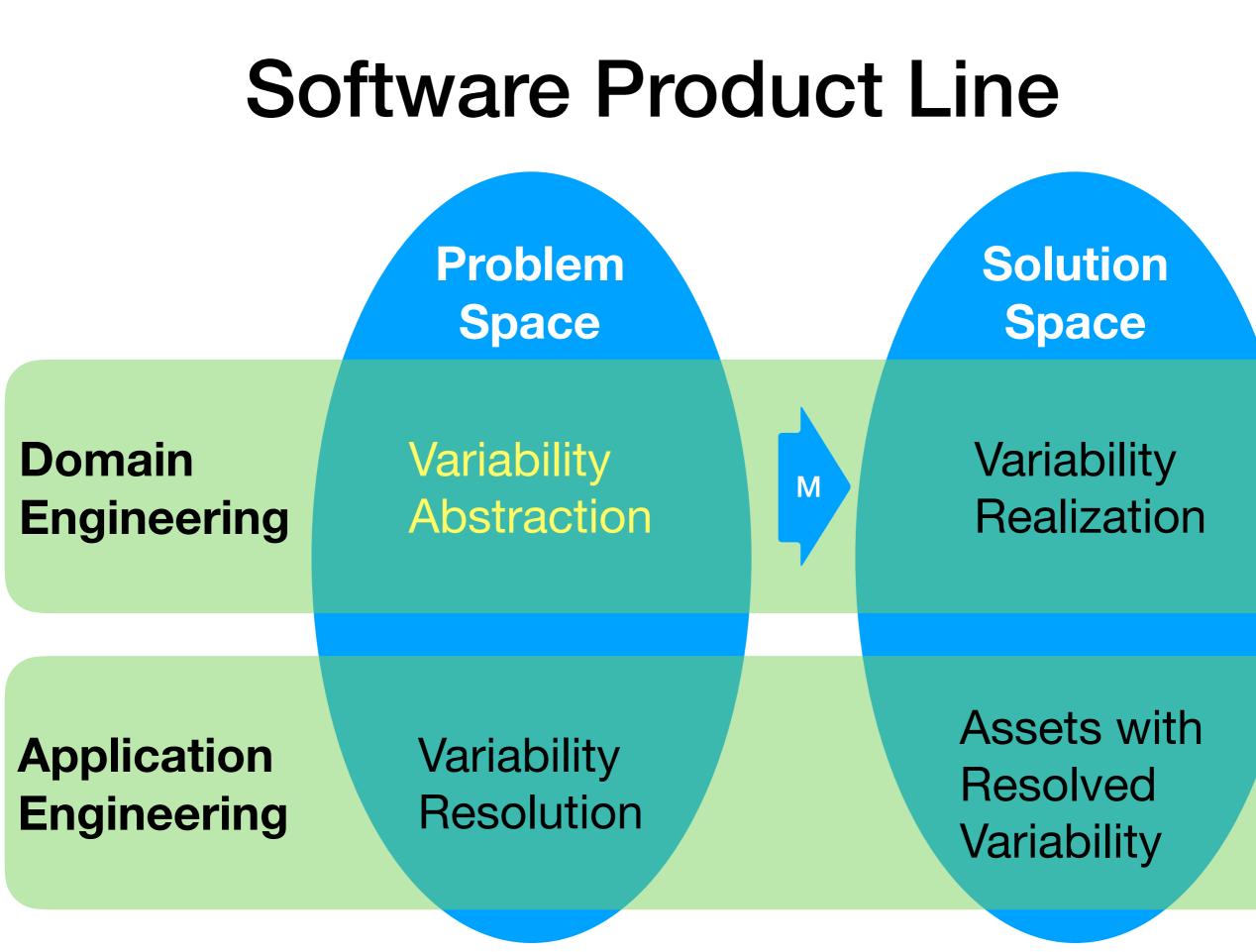
#### Feature models

- Domain Specific Languages
- none
- Feature model configuration, constraints
- Domain specific model
- XML, JSON, custom text format, ...
- Code (with variability techniques)
- Code generators
- Model transformers
- Parts may use DSLs

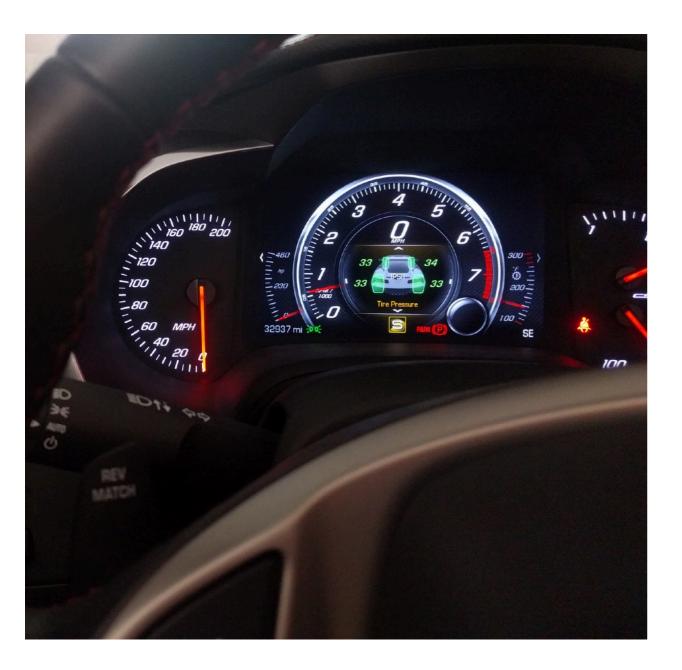
# **Spectrum of Variability Architectures**

Stay as Close to the Left as Possible Exploit Commonality Manage Variability

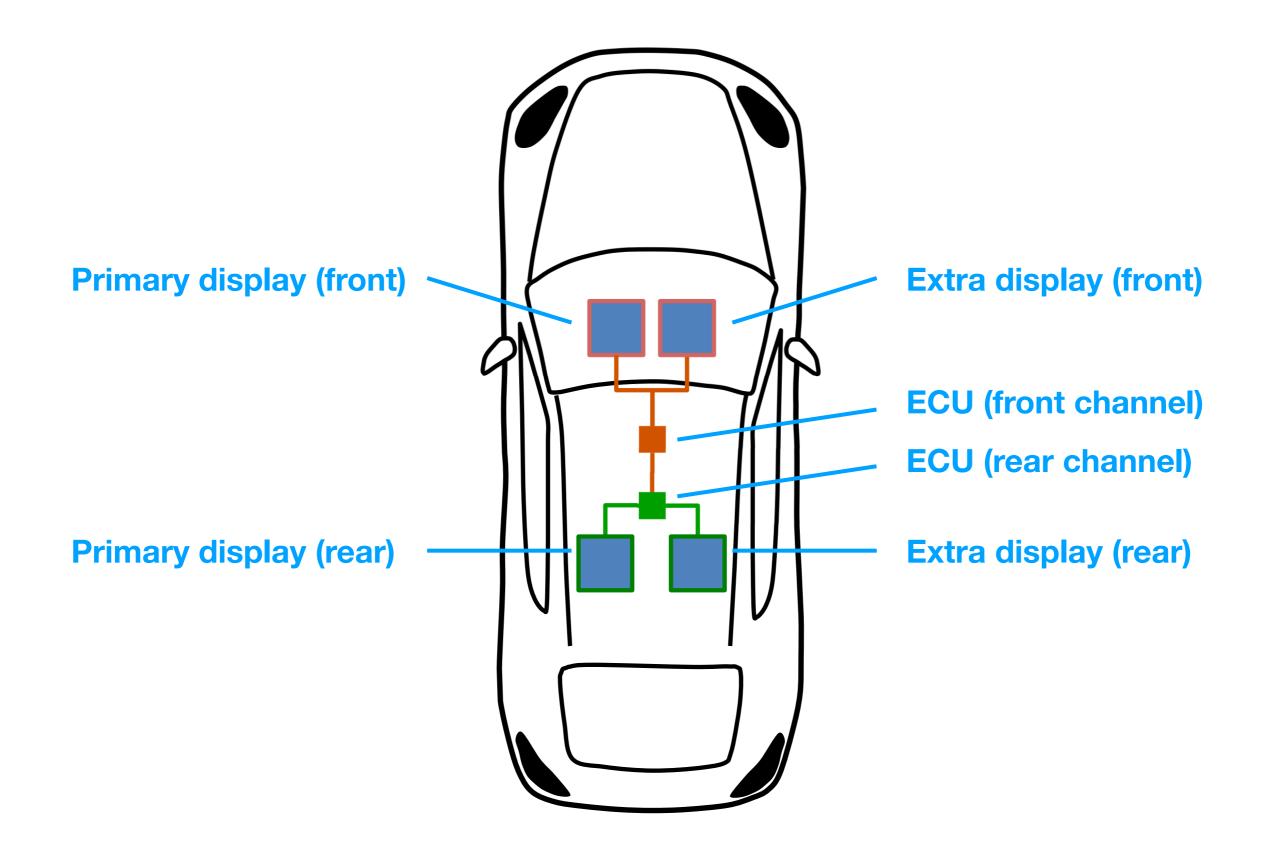




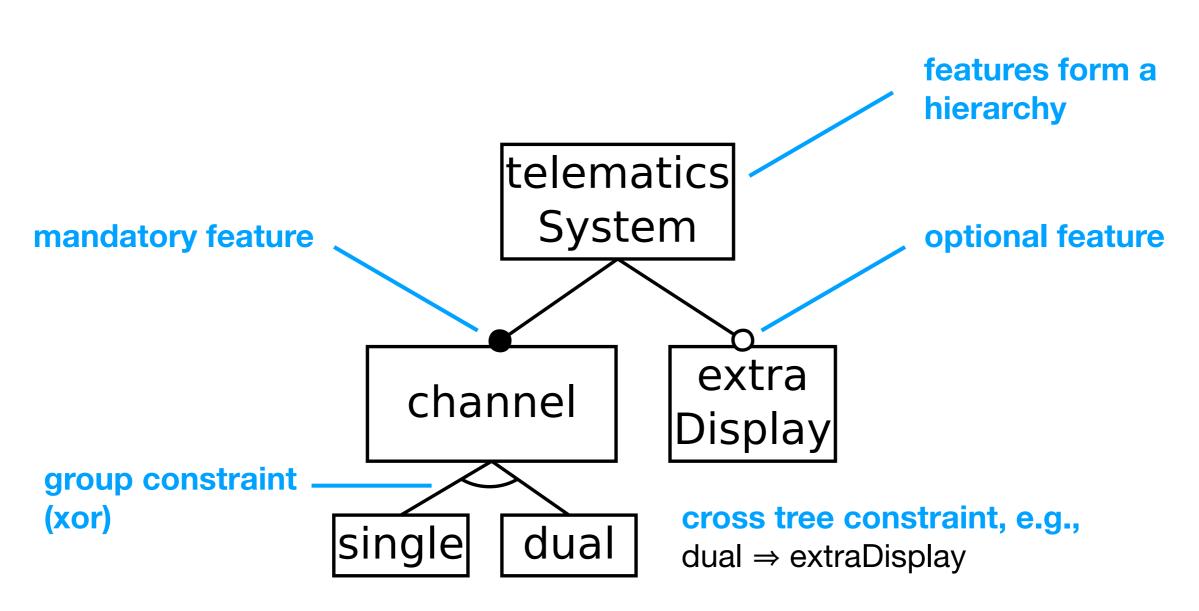
### **Example: Telematics System**



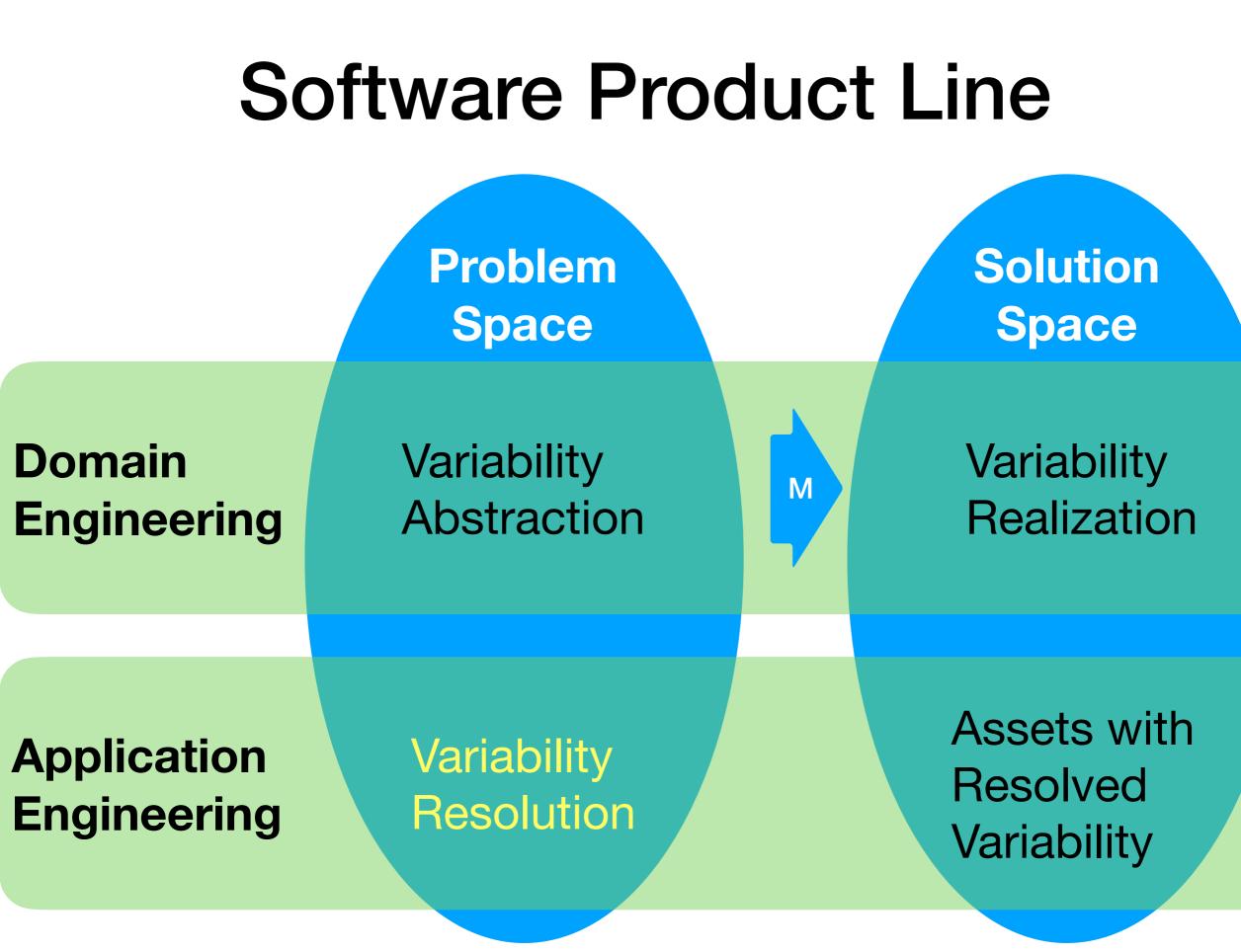




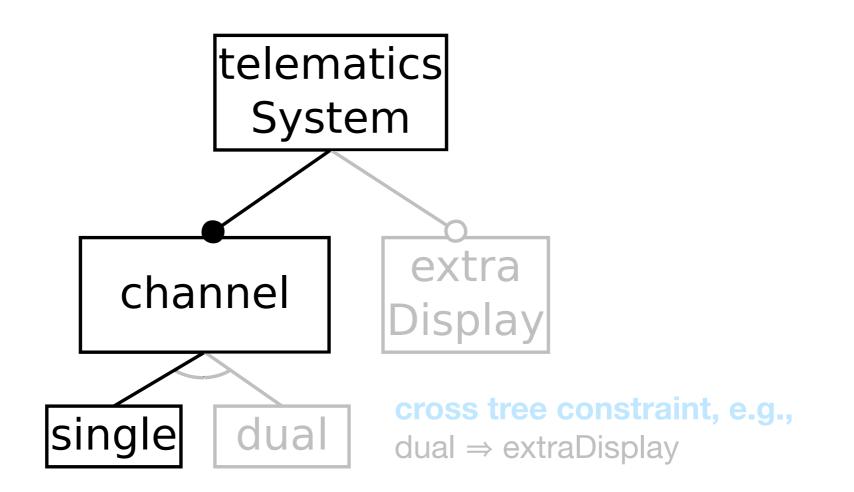
### **Feature Model**



single kind of relationship: *subfeature* meaning: *implication* 



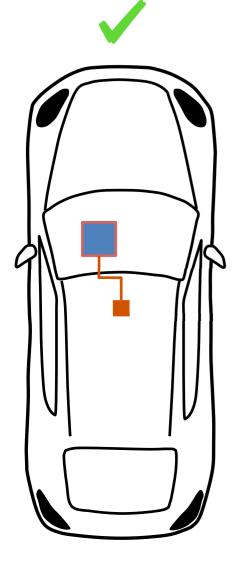
## **Feature Configuration**

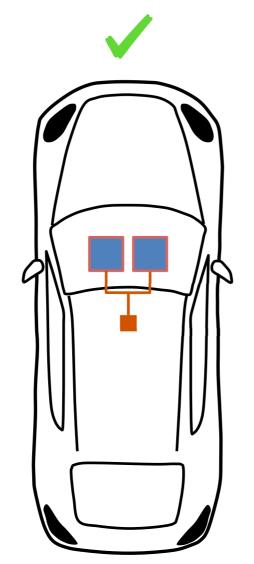


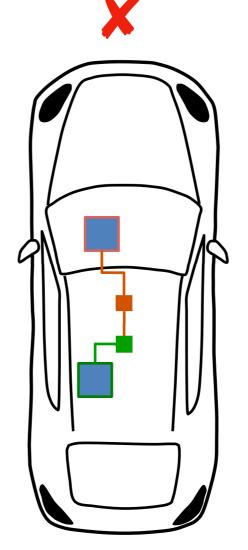
{ telematicsSystem,
 channel, single }

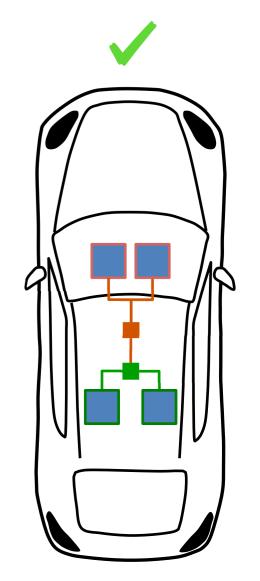
# **Configuration Semantics of an FM**

#### Set of Configurations









{ telematicsSystem,
 channel, single }

{ telematicsSystem,
 channel, single,
 extraDisplay }

{ telematicsSystem, channel, **dual** }

{ telematicsSystem,
 channel, dual,
 extraDisplay }

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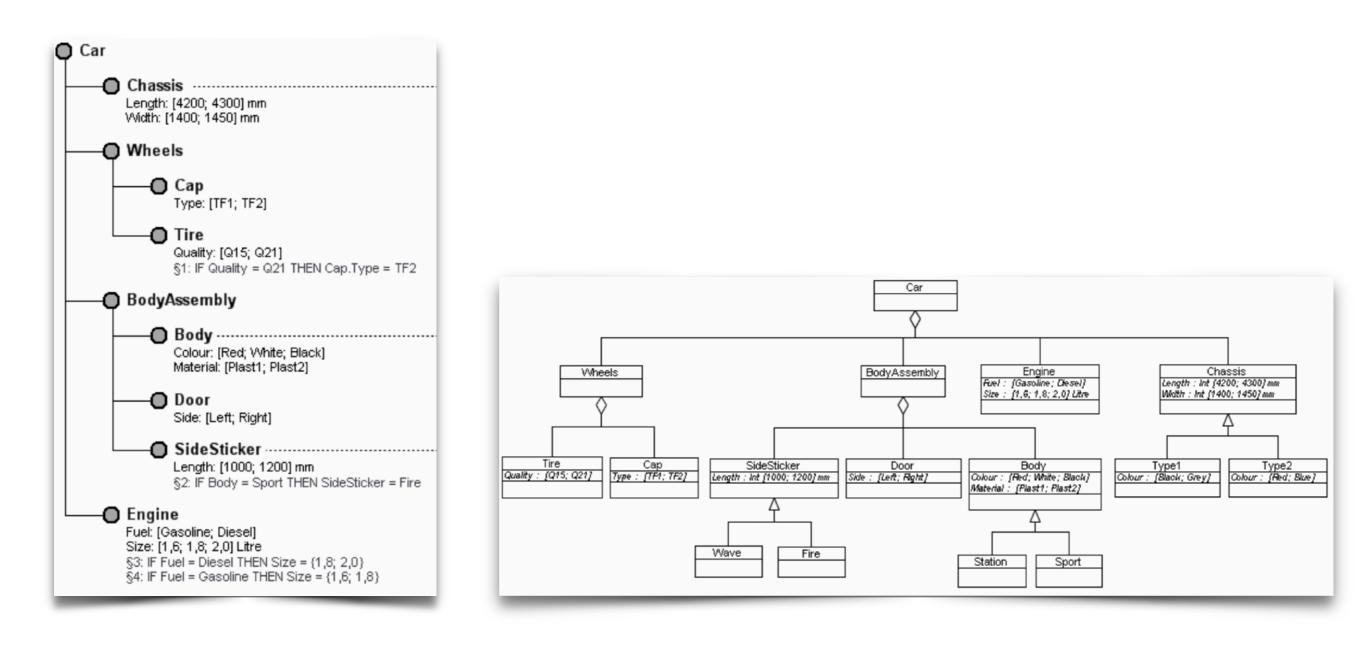
# Feature Modeling and FODA

- FODA succeeds for its **simplicity**
- Probably best intro in Czarnecki's Generative Programming (Ch. 4)
- 4700+ citations, **never formally published**

Google	Feature Oriented Domain Analysis	
Scholar	About 3,060,000 results (0.10 sec)	
Articles	Feature-oriented domain analysis (FODA) feasibility study KC Kang, SG Cohen, JA Hess, WE Novak, AS Peterson - 1990 - dtic.mil	
Case law	Abstract: Successful Software reuse requires the systematic discovery and exploitation of commonality across related software systems. By examining related software systems and	
My library	the underlying theory of the class of systems they represent, <b>domain analysis</b> can provide a Cited by 4704 Related articles All 14 versions Cite Save	

# Feature Models vs Class Models

A Feature Model in Product Variant Master Notation (Hvam)



Haug et al., Creating a documentation system to support the development and maintenance of product configuration systems, WSEAS 2007

# Feature Models vs Class Models

Many and complex: class.

Concepts	<b>Few and simple</b> : feature, subfeature, group, constraint	generalization, composition, association, redefinition, refinement, property, multiplicity, package, data type, primitive type, enumeration,
Use	<b>Variation</b> of <i>user-relevant</i> characteristics of product variants	<b>Concepts</b> representing more detailed aspects of products; <i>product line architectures</i>
Semantics	<b>Configuration</b> - selections from <i>predefined choices</i> within a fixed tree structure	<b>Instantiation</b> - making <i>new</i> structures that conform to predefined types, and <i>connecting</i> them via links

Bak et al., Clafer: Unifying Class and Feature Modeling, SOSYM 2014

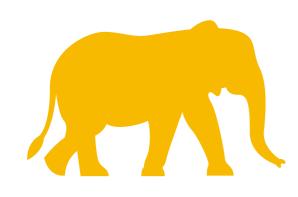
# How to Build Feature Models?

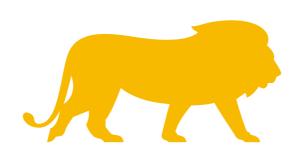
**Bottom Up - Incremental Adoption** 

- Identify **cloned** code/functionality
- Find the **patches** that describe differences
- Diffs → variation points
- Aggregate variation points into hierarchical features

Jepsen et al., Minimally Invasive Migration to Software Product Lines, SPLC 2007 Berger et al., A survey of variability modeling in industrial practice, VAMOS 2013

# Variability Modeling in the Wild

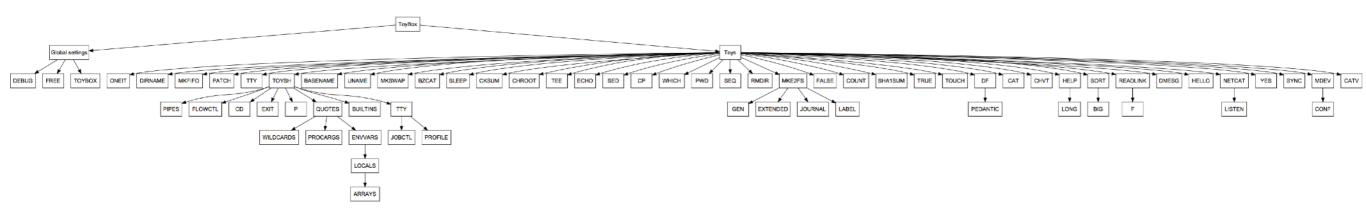






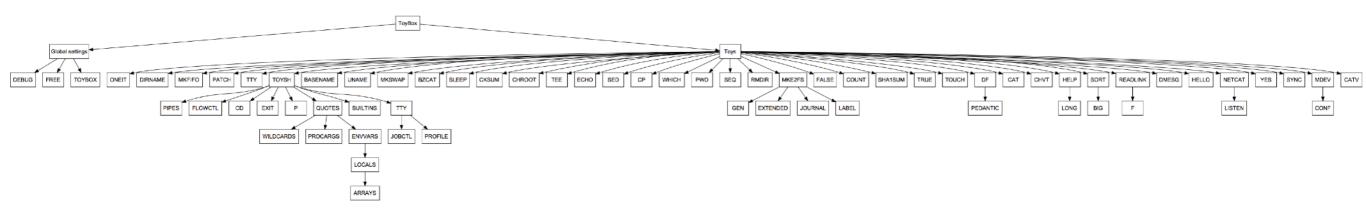
## Healthy Wild Variability Model Club

**ToyBox Project, 71 Features** 



## Healthy Wild Variability Model Club

**ToyBox Project, 71 Features** 



The Linux Kernel has 6-12k features, depending on how you count. Max depth: 8. Most leaves are at 4! **Shallow** 

 $\downarrow$  this is the Linux Kernel model fit to the slide width  $\downarrow$ 

Berger et al., A Study of Variability Models and Languages in the Systems Software Domain, TSE 2013

## **KConfig & CDL**

#### FM with Attributes, Defaults, Constraint Propagation, UI, ...

otion	Name	Value
HAVE_ARCH_EARLY_PFN_TO_NID	HAVE_ARCH_EARLY_PFN_TO_NID	N
Power management and ACPI options		
ARCH_HIBERNATION_HEADER	ARCH_HIBERNATION_HEADER	N
> 🗹 Power Management support	PM	Υ
PM_TRACE	PM_TRACE	Υ
Suspend/resume event tracing	PM_TRACE_RTC	Υ
☑ PM_SLEEP_SMP	PM_SLEEP_SMP	Υ
PM_SLEEP	PM_SLEEP	Υ
➤ ☑ Suspend to RAM and standby	SUSPEND	Υ
> I Hibernation (aka 'suspend to disk')	HIBERNATION	Υ
Advanced Power Management Emulation	APM_EMULATION	Ν
> ☑ ACPI (Advanced Configuration and Power Interface) Support	ACPI	Υ
X86_APM_BOOT	X86_APM_BOOT	Ν
<ul> <li>APM (Advanced Power Management) BIOS support</li> </ul>	APM	N
<ul> <li>CPU Frequency scaling</li> </ul>		
> ☑ CPU idle PM support	CPU_IDLE	Υ
<ul> <li>Memory power savings</li> </ul>		
Bus options (PCI etc.)		

defined at kernel/power/Kconfig:1

"Power Management" means that parts of your computer are shut off or put into a power conserving "sleep" mode if they are not being used. There are two competing standards for doing this: APM and ACPI. If you want to use either one, say Y here and then also to the requisite support below.

ROM filesystem	v3_0	<u>^</u>	ltem		Conflict	
	v3_0		CYGPK	G_POSIX_CLOCKS	Unsatisfie	ed
	ad			 G_DEVS_FLASH_FF		ed
📷 jffs2 gc thread priority	30		CYGPK	G_FILEIO_FNMATCH	l Unsatisfie	ed
📷 jffs2 gc stackstack size	8192		CYGPK	G_DEVS_FLASH_AN	1D_A Unsatisfie	۶d
🌉 ticks between each garbage collect	100					_
Include write support for JFFS2						[
Support for NAND flash			Property			
酸 Debug level	0		URL	ref/fileio.html		
✓ ☑ Compress data			Macro		_JFFS2_COMPR	
Compress data using zlib			File Enabled	0	/workspace/ecos/	bui
Compress data using rtime			Flavor	bool		
Compress data using rubin			DefaultV			
E Set the default compression mode	PRIORITY		Define	JFFS2 COM	PRESSION	
Memory pool size	0	≡		_		
Additional compiler flags	-D_ECOS					[
Suppressed compiler flags			Compres	sion and decompres	sion are entirely	
Support for fileio's struct dirent d_type field				by the file system and		ren
JFFS2 FS tests	tests/jffs2_1 test	s/jffs2_2 tes		ons. However, select	0	
▽ 🚰 Linux compatibility layer	v3_0			unt of RAM required a operations consider		
😥 Define page size	12		CPU.	operations consider	abiy ii you nave a	510
▽ 😫 Zlib compress and decompress package	v3_0					
Should deflate() produce 'gzip' compatible ou	itpu	~ ~				





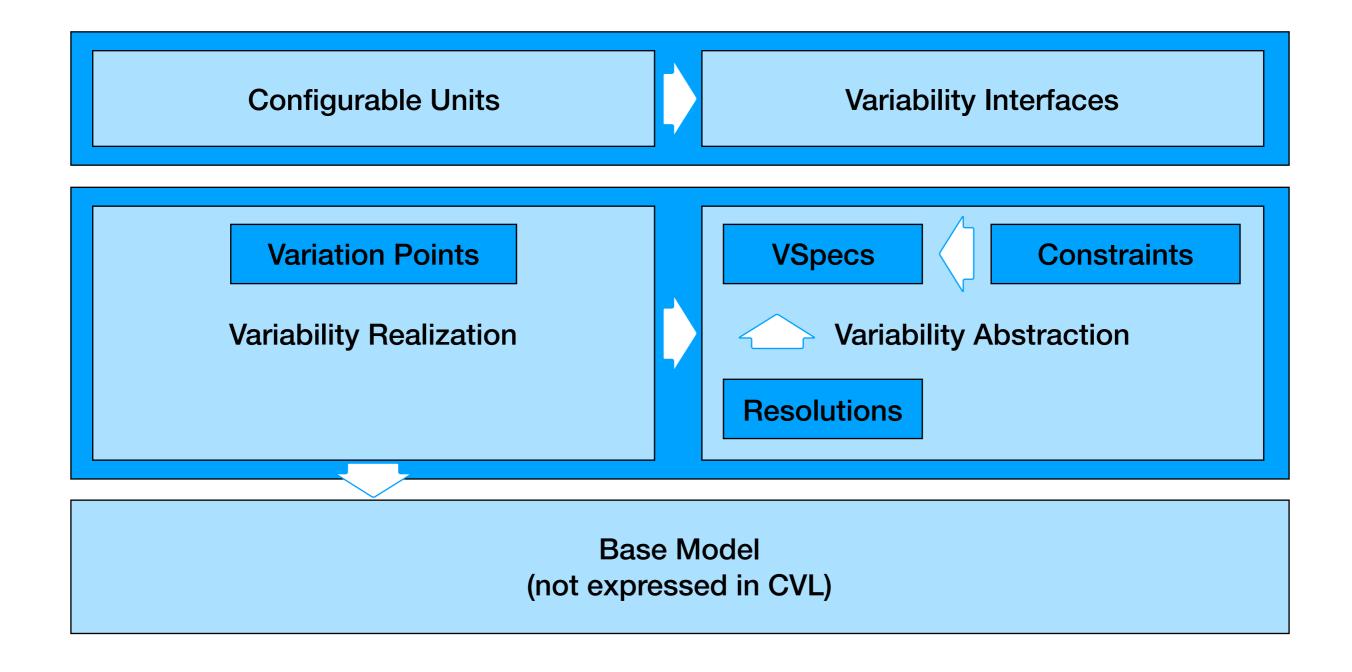
# **KConfig & CDL**

### **Textual Variability Models**

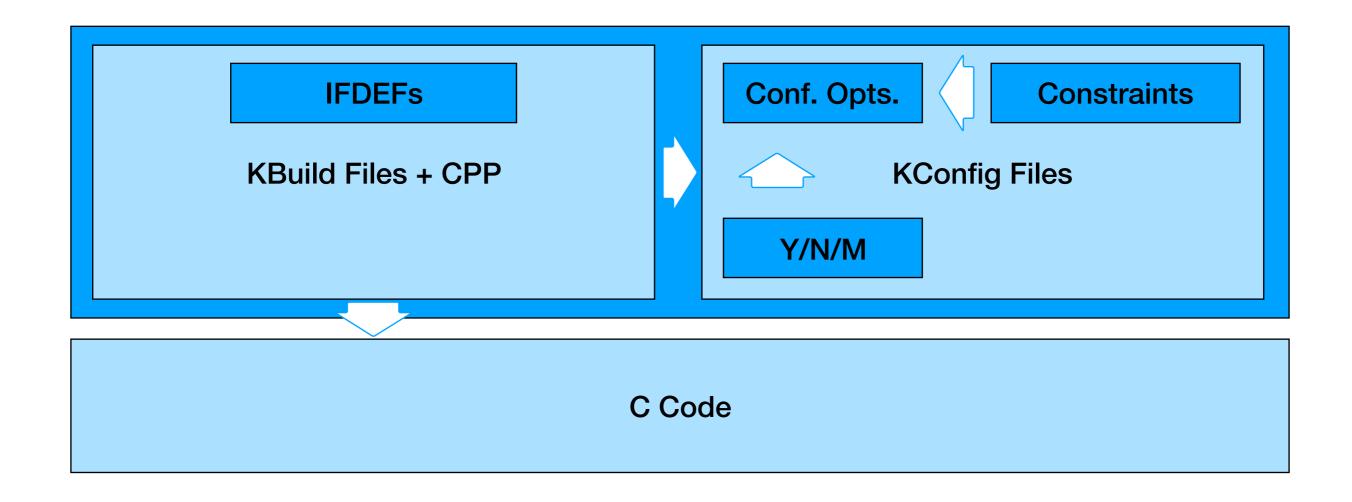
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Option	Name	Value	<u>File Edit View Build Tools H</u> elp				
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<ul> <li>Power management and ACPI options</li> </ul>					0		
ARCH_HIBERNATION_HEADER	ARCH_HIBERNATION_HEADER	N	ROM filesystem	v3_0	<u>^</u>	ltem	Conflict P
> 🔽 Power Management support	PM	Y	✓ 😫 JFFS2 filesystem	v3_0		CYGPKG_POSIX_CL	LOCKS Unsatisfied Re
PM_TRACE	PM_TRACE	Y		nd thread		CYGPKG_DEVS_FL	ASH_FRV_F Unsatisfied Re
Suspend/resume event tracing	PM_TRACE_RTC	Y	iffs2 gc thread priority	30		CYGPKG_FILEIO_FI	MATCH Unsatisfied Re
☑ PM_SLEEP_SMP	k-1 menuconfig MISC_FILESYSTEMS		c-1 cdl_component MISC FILESYSTEMS {				ASH_AMD_A Unsatisfied Re
PM_SLEEP	k-2 bool "Miscellaneous filesystems"		o-2 <b>display</b> "Miscellaneous filesyst	ems"			
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	k-4 if MISC_FILESYSTEMS		c-4 } c-5			Property Value	2
Advanced Dewer Management Emulation	k-5 k-6 <b>config</b> JFFS2 FS		c-5 c-6 <b>cdl_package</b> CYGPKG_FS_JFFS2 {				eio.html
<ul> <li>ACPI (Advanced Configuration and Power Interfac</li> </ul>	k-7 tristate "Journalling Flash File Sys	stem" <b>if</b> MTD	c-7 <b>display</b> "Journalling Flash Fi	le System"			
	k-8 select CRC32 if MTD		0-8 requires CYGPKG_CRC				OPT_FS_JFFS2_COMPRESS
> □ APM (Advanced Power Management) BIOS suppo	k-9		<ul> <li>c-9 implements CYGINT_IO_FILEIO</li> <li>c-10 parent MISC FILESYSTEMS</li> </ul>				e/berger/workspace/ecos/build/
<ul> <li>CPU Frequency scaling</li> </ul>	e10		c-11 active_if MTD			Enabled True	
> ☑ CPU idle PM support	-12		c-12			Flavor bool	
Memory power savings	config JFFS2_FS_DEBUG		or13 cdl_option CYGOPT_FS_JFFS2_DEBU or14 display "Debug level"	IG {		DefaultValue 1	
Bus options (BCLots)	int "JFFS2 Debug level (0=quiet, 2=nd depends on JFFS2 FS	bisy)	c-14 <b>display</b> "Debug level" c-15 <b>flavor</b> data			Define JFFS	2_COMPRESSION
	default 0		o-16 default_value 0		=		
	-17 range 0 2		c-17 legal_values 0 to 2				4
	-18 help -19 Debug verbosity of		₀-18 define CONFIG_JFFS2_FS ₀-19 description "Debug verbosit			Compression and de	compression are entirely
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· k	config JFFS2_FS_WRITEBUFFER		cdl_option CYGOPT_FS_JFFS2_NAND	) {			equired and slows down read
defined at leave all a superficient	6-23     bool       6-24     depends on JFFS2_FS		c-23 flavor bool c-24 define CONFIG_JFFS2_FS				considerably if you have a slow
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Power Management" means that parts of your computer			c-26 }				
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eing used. There are two competing standards for doing		or JFFS2"	c-29 display "Compress data"	-			
nd ACPI. If you want to use either one, say Y here and th	depends on JFFS2_FS		o-30 default_value 1				
	-31		0-31				
	config JFFS2_ZLIB bool "Compress w/zlib" if JFFS2_CC	OMPRESS	cdl_option CYGOPT_FS_JFFS2_C c33 display "Compress da	ta using zlib"	-		4 conflicts
	-34 depends on JFFS2_FS		c-34 requires CYGPKG_COM				
	select ZLIB_INFLATE		c-35 default_value 1				
	-36 <b>default</b> y -37		c-36 } c-37				
	-37 -38 <b>choice</b>		o-37 o-38 cdl_option CYGOPT_FS_JFFS2_C	OMPRESS CMODE {			
	A39 prompt "Default compression" if JFFS2	2_COMPRESS		ault compression mode"			
	40 default JFFS2_CMODE_PRIORITY		c-40 flavor data	,			
	41 depends on JFFS2_FS 42 config JFFS2_CMODE_NONE		o-41 default_value { "PRIORITY" o-42 legal_values { "NONE" "PR	} IORITY" "SIZE" }			COS
	c42 config JFFS2_CMODE_NONE bool "no compression"		<pre>c-42 legal_values { "NONE" "PR c-43 }</pre>	JUNITI SIZE }			
	config JFFS2_CMODE_PRIORITY		0-44 }				
	45 bool "priority"		c-45 }				
	<pre>config JFFS2_CMODE_SIZE bool "size (EXPERIMENTAL)"</pre>		c-46 c-47				
	-47 bool "size (EXPERIMENTAL)" -48 <b>endchoice</b>		6-4/ c-48				
	-49 endif		c-49				

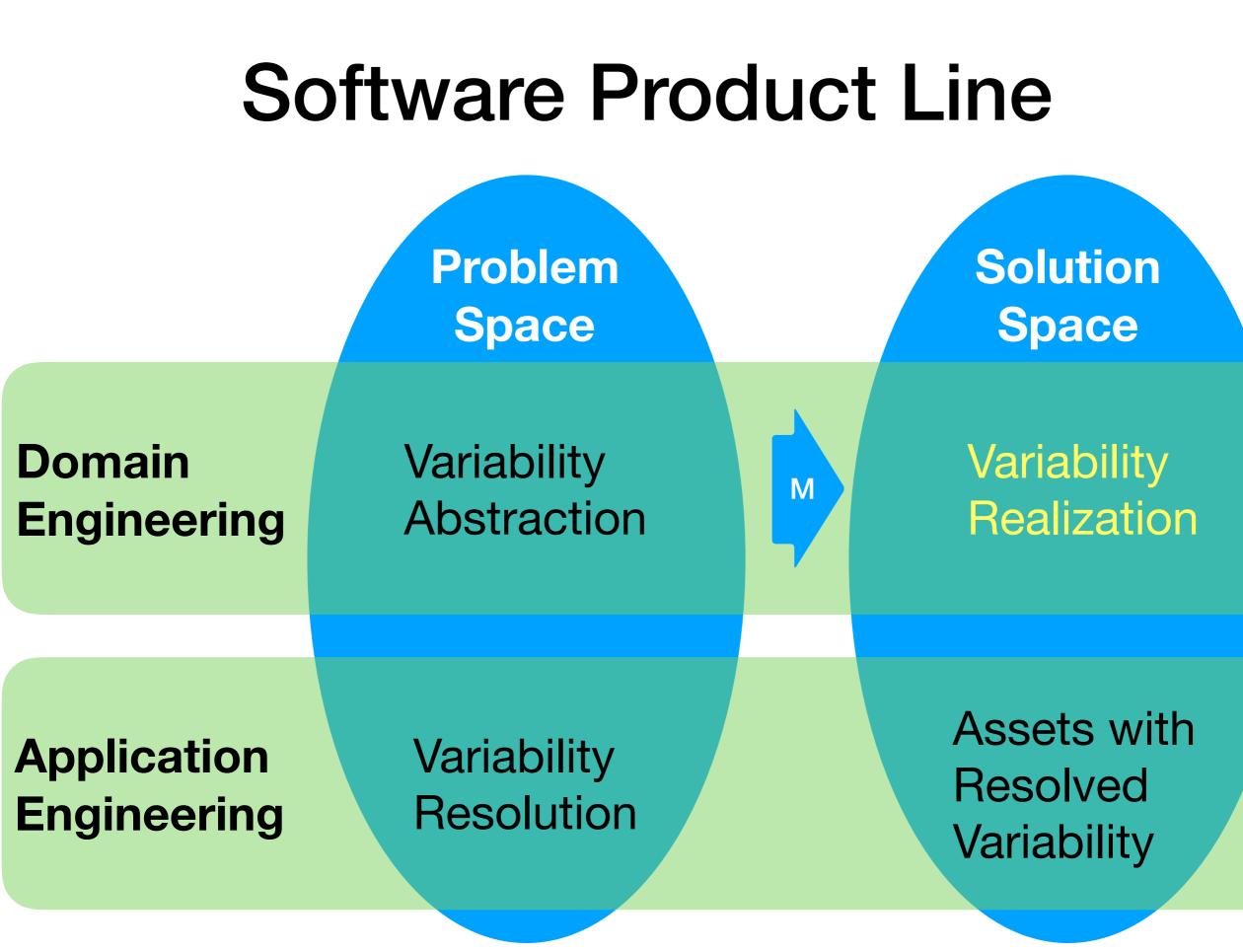
# Common Variability Language (CVL)

IBM et al., Proposal for CVL Revised Submission, 2012



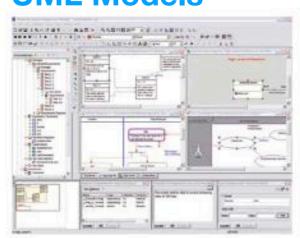
## **CVL Architecture for Linux Junkies**





#### **UML Models**

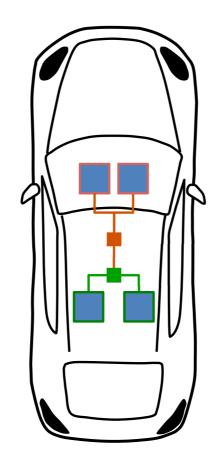






#### **Calibrations**

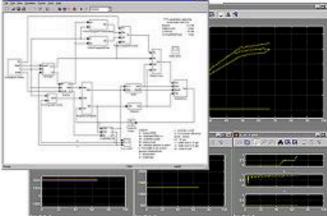
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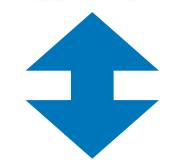
#### **HW/SW Mapping**

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## Hybrid Models

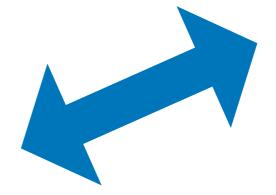






#### **OS Generation**

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## Software Product Line

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Problem Space

Domain Engineering

## Variability Abstraction

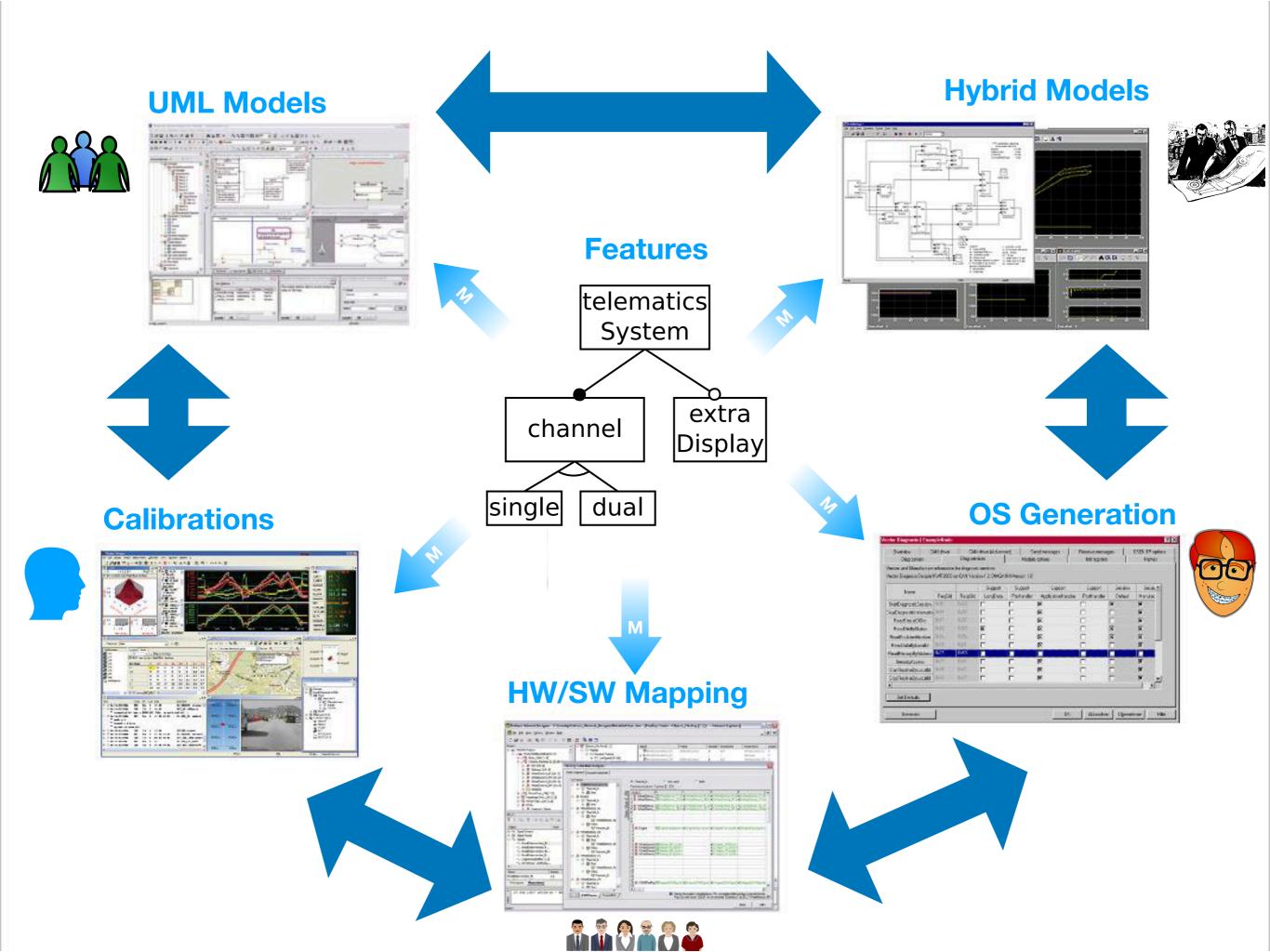
Solution Space

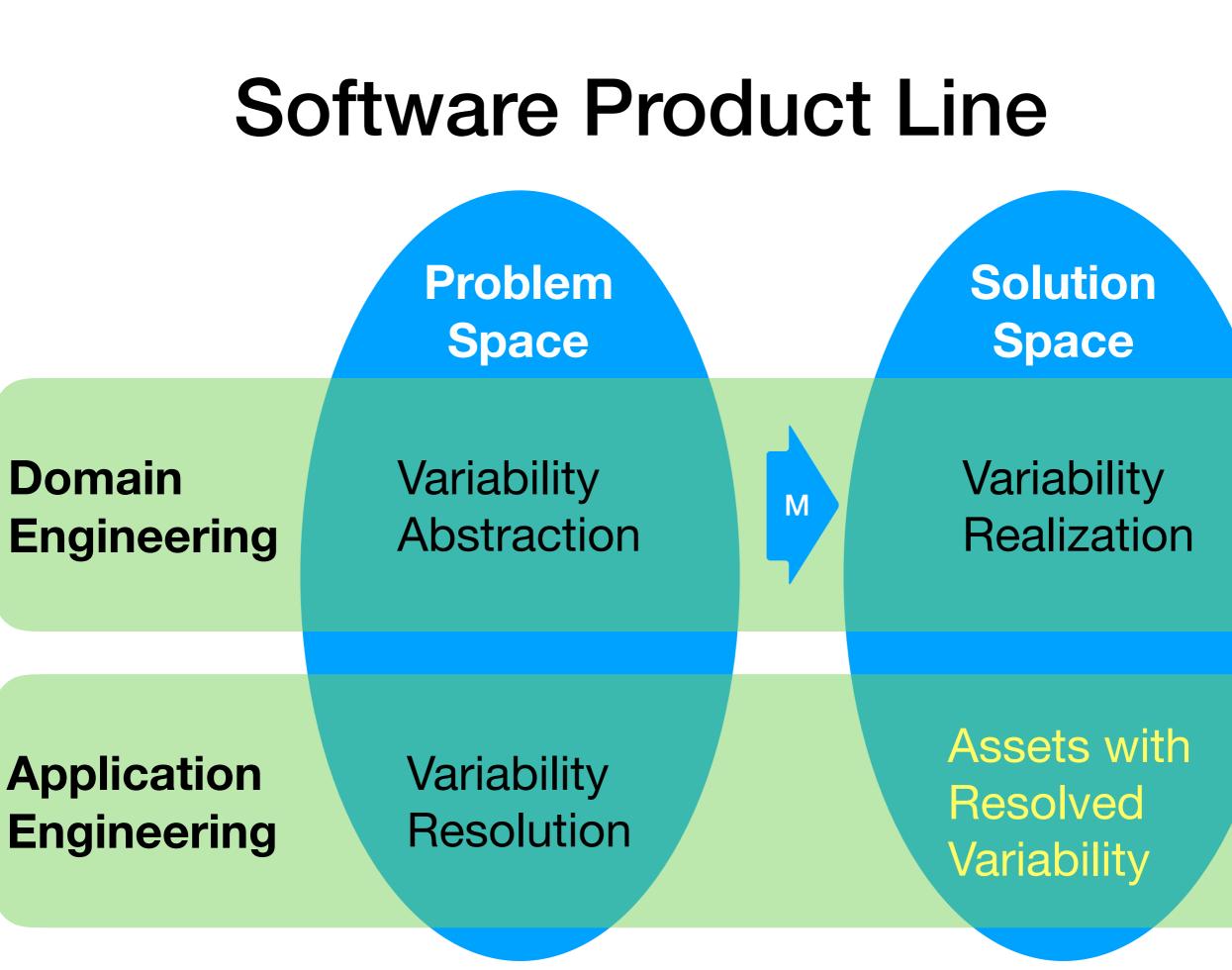
Variability Realization

Application Engineering

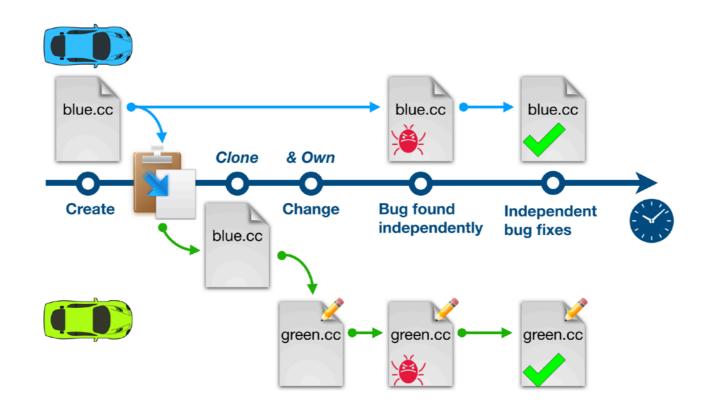
## Variability Resolution

Assets with Resolved Variability

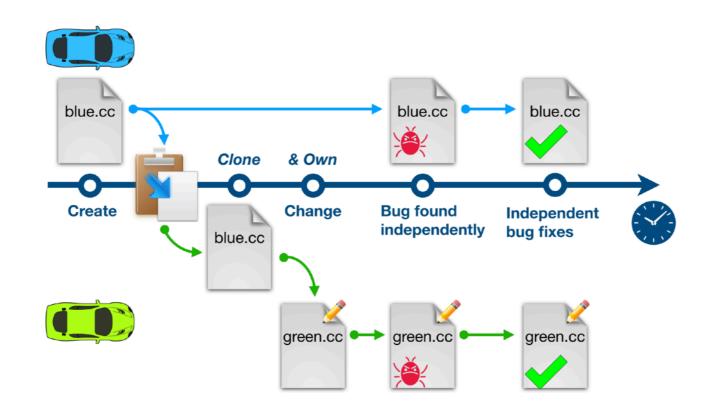




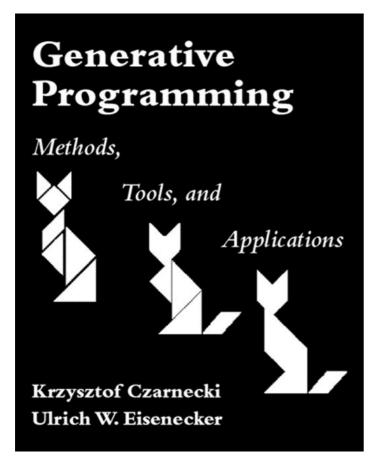
## Is Clone and Own Always Bad?

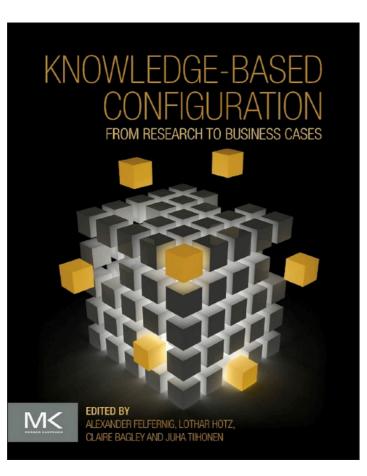


## Is Clone and Own Always Bad?



# not if the cost of cloning is less than the cost of an SPL 😏





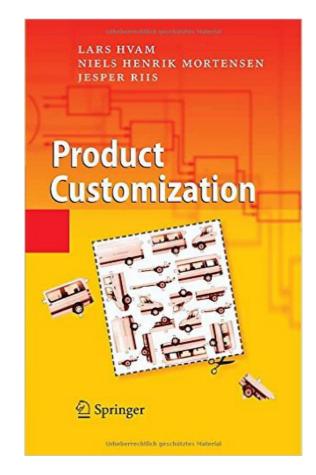
**WILEY** 

## Model-Driven Software Development

Technology, Engineering, Management

Thomas Stahl, Markus Völter with Jorn Bettin, Arno Haase and Simon Helsen Foreword by Krzysztof Czarnecki Translated by Bettina von Stockfleth





# Exercise

- Example Domain: *Traffic Lights* 
  - Feature-oriented commonality/variability analysis
  - Domain concept analysis
  - Application configuration
- Apply Example-Driven Modeling
- Use Clafer & Web Tools
  - Tutorial style
  - Hands-on
  - Small exercises

# Interactive Tutorial

http://t3-necsis.cs.uwaterloo.ca:8098/

Use Chrome or Firefox

Indent code with spaces, not tabs