

DSM TP 2017

**8th International Summer School
on Domain-Specific Modeling
Theory and Practice**

**Montreal, Canada
10-14 July 2017**

Variability / Product Families

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Currently: The MathWorks, Inc.
Previously: GSD Lab, University of Waterloo

Acknowledgments

slides based on tutorials by

Andrzej Wąsowski, IT University of Copenhagen

Michał Antkiewicz, University of Waterloo

Krzysztof Czarnecki, University of Waterloo



Software-intensive Products Come in Many Variants



Domain Engineering

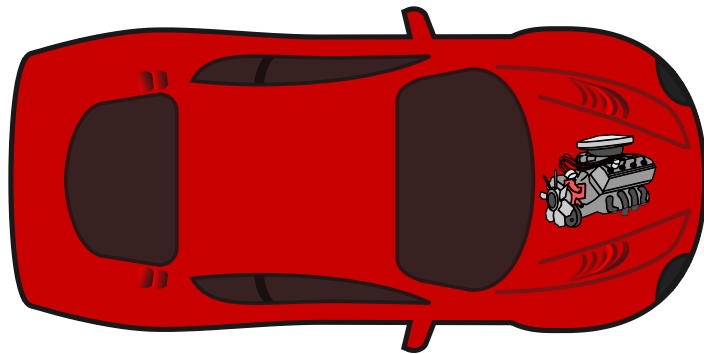
aka Product Line Engineering

aka Product Family Engineering

Application Engineering

Development *with* Reuse

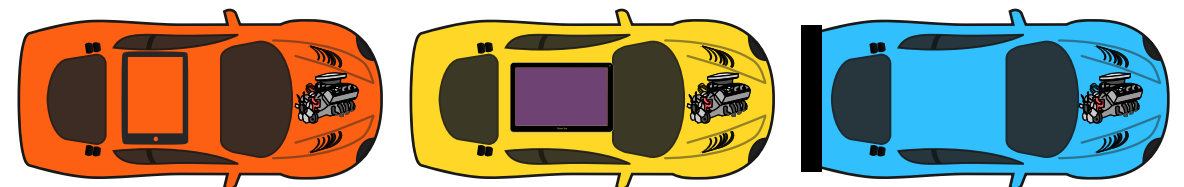
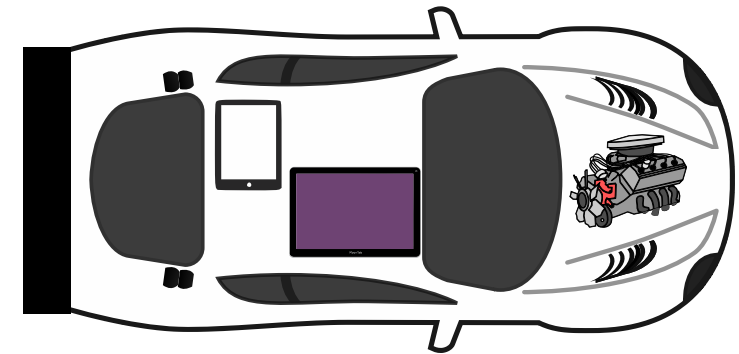
Single Product



Domain Engineering

Development *for* Reuse

Product Family



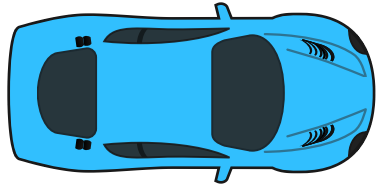
Why Domain Engineering?

Why Domain Engineering?

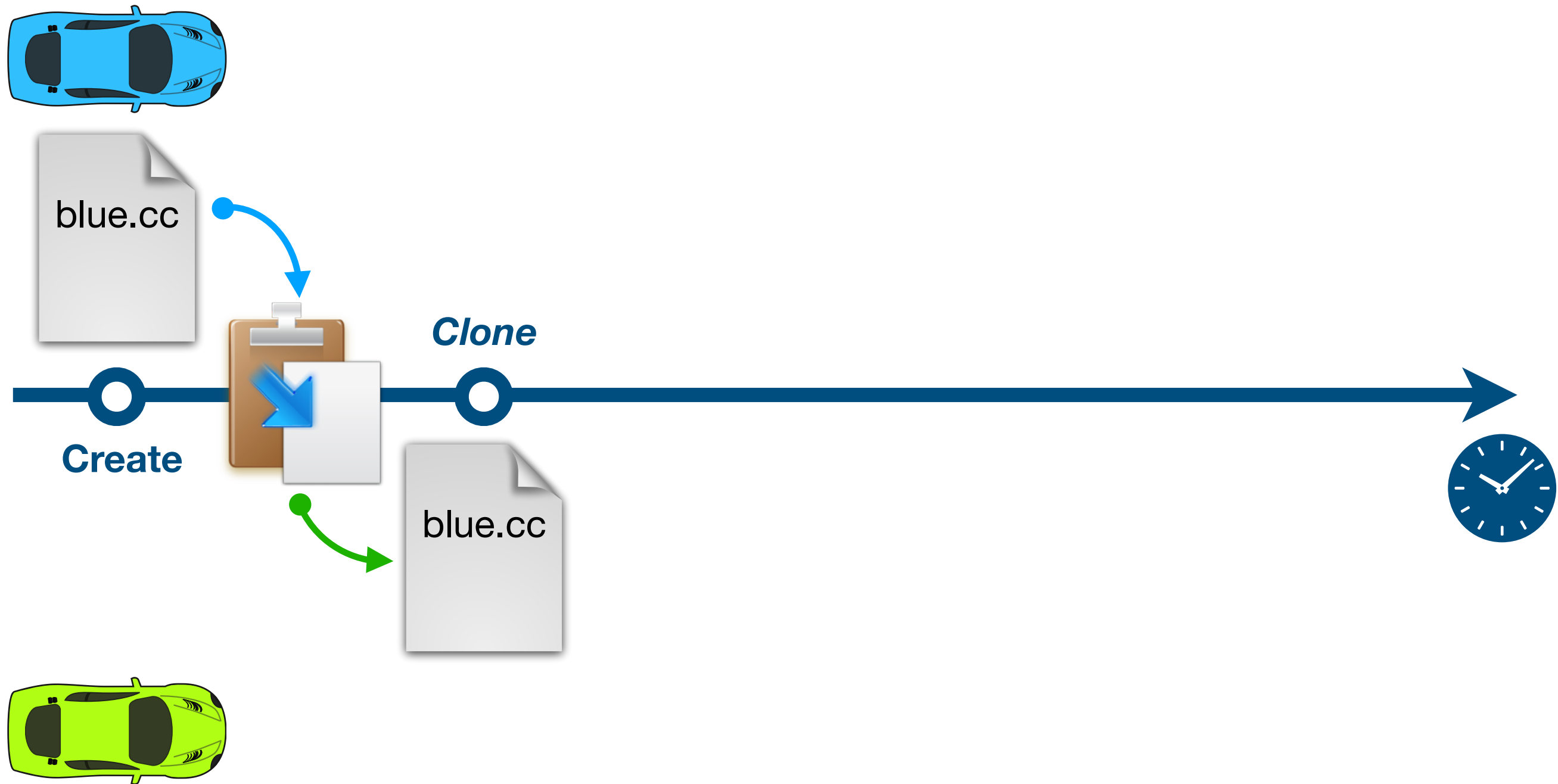
...because

opportunistic reuse *does not scale*

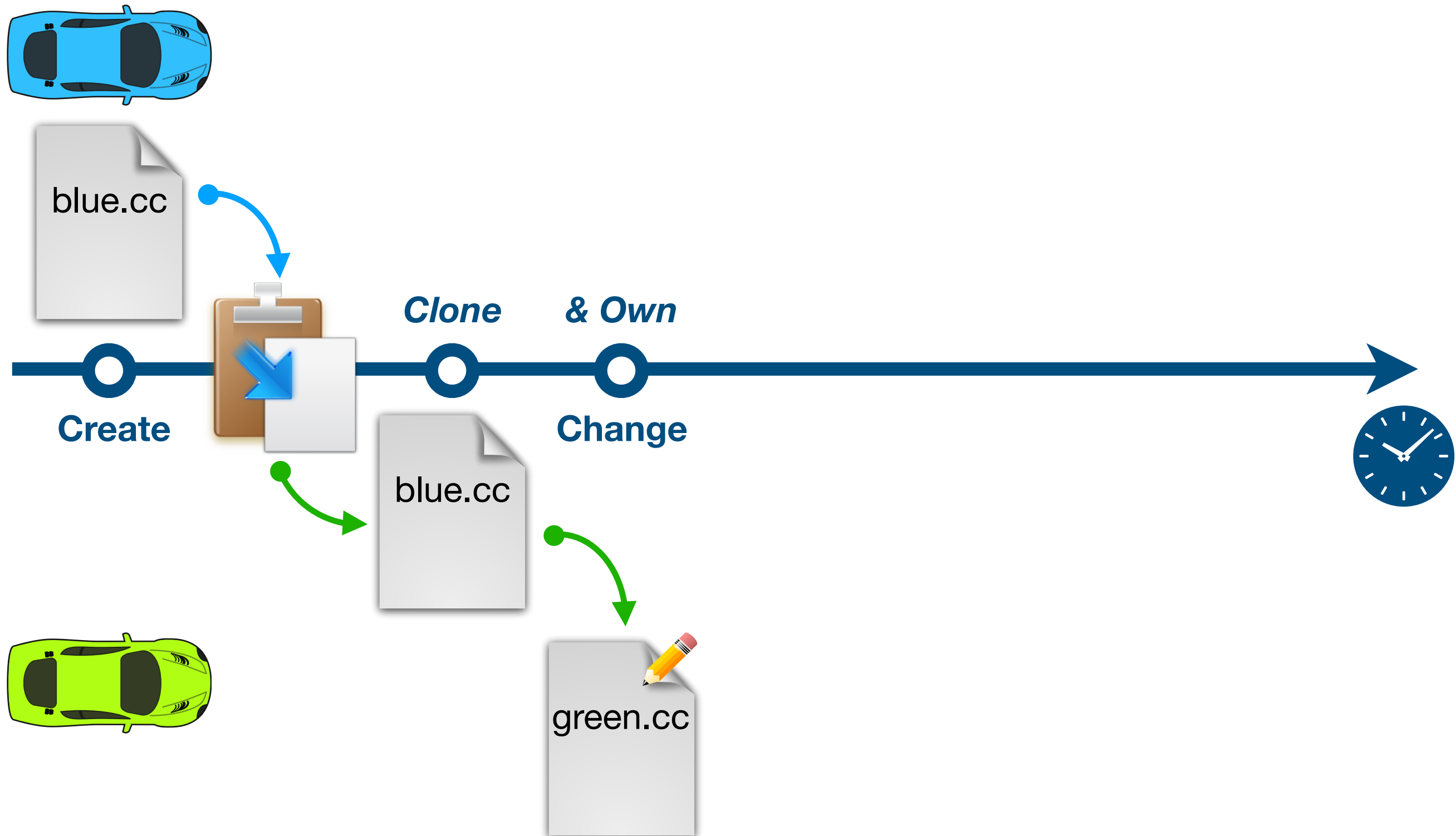
Clone and Own



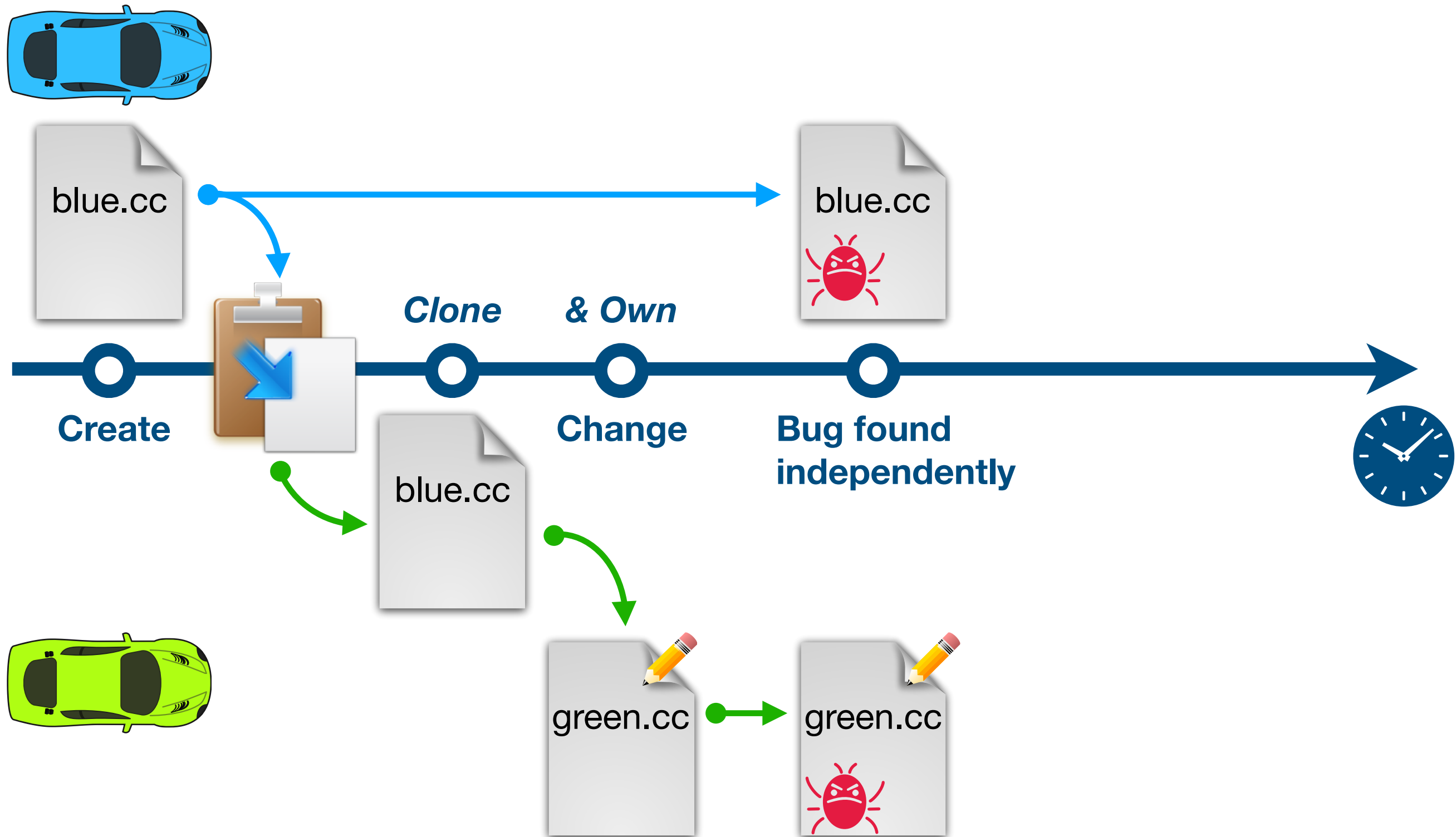
Clone and Own



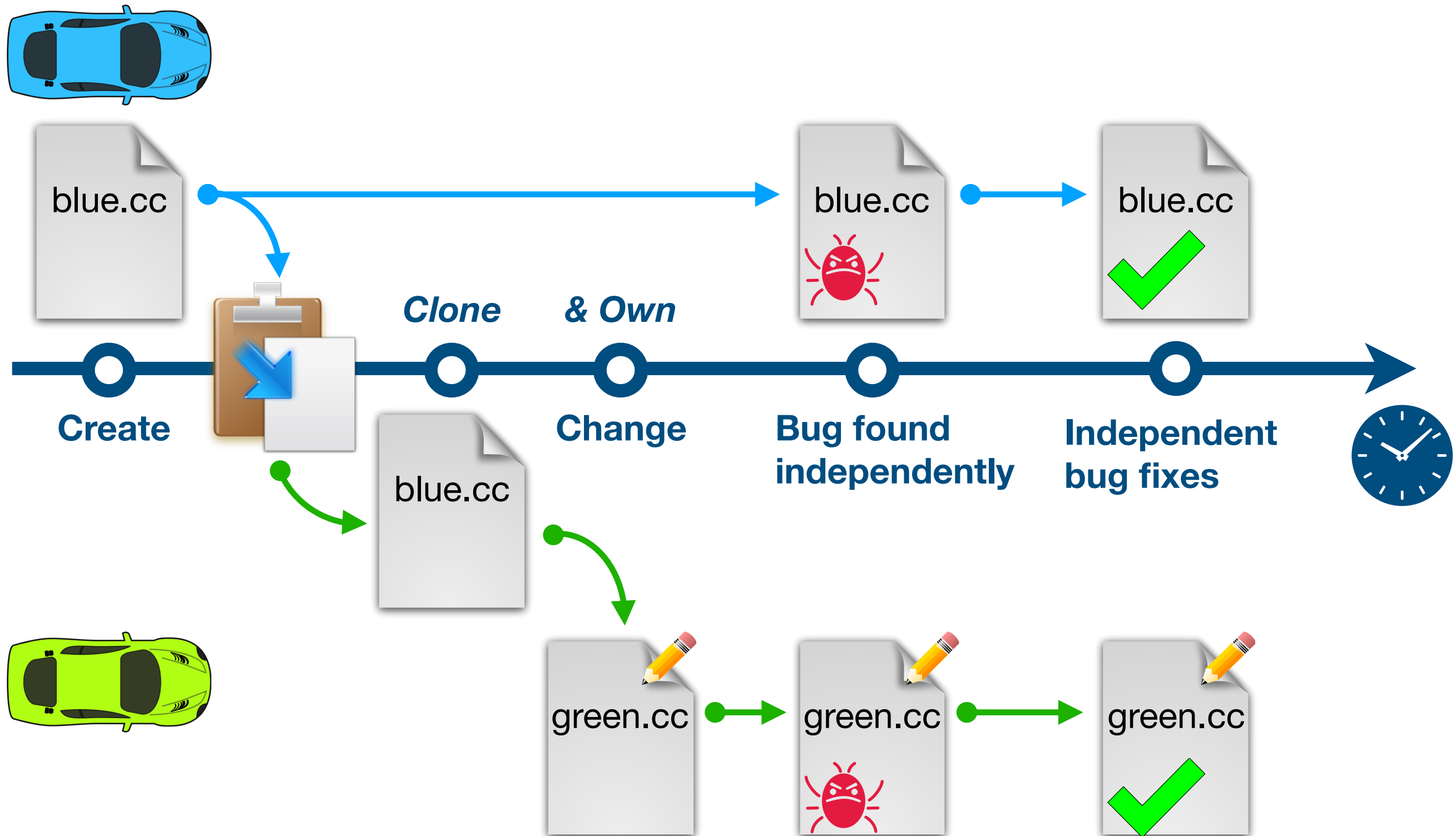
Clone and Own



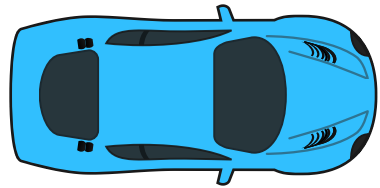
Clone and Own



Clone and Own



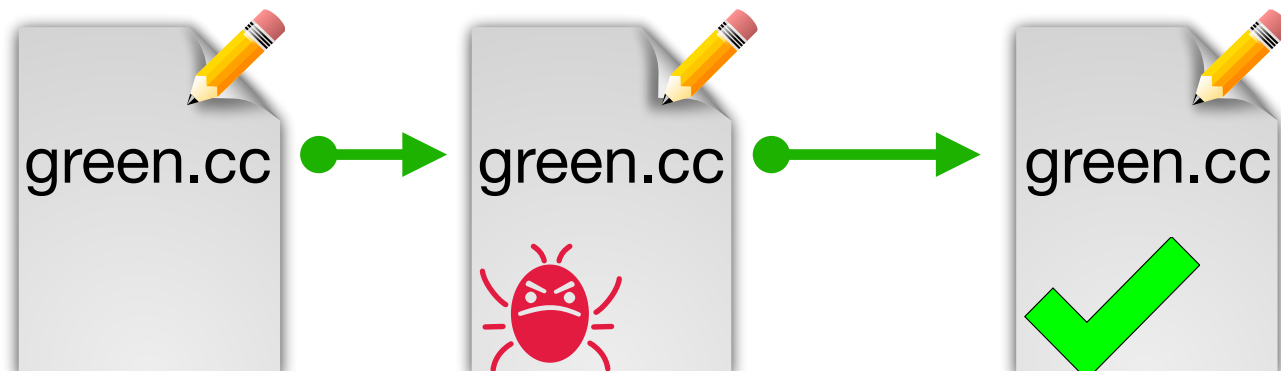
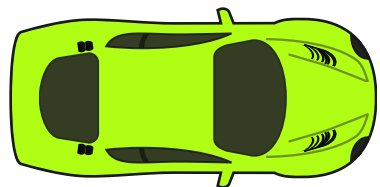
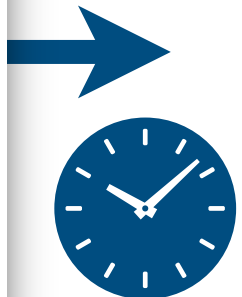
Clone and Own



blue



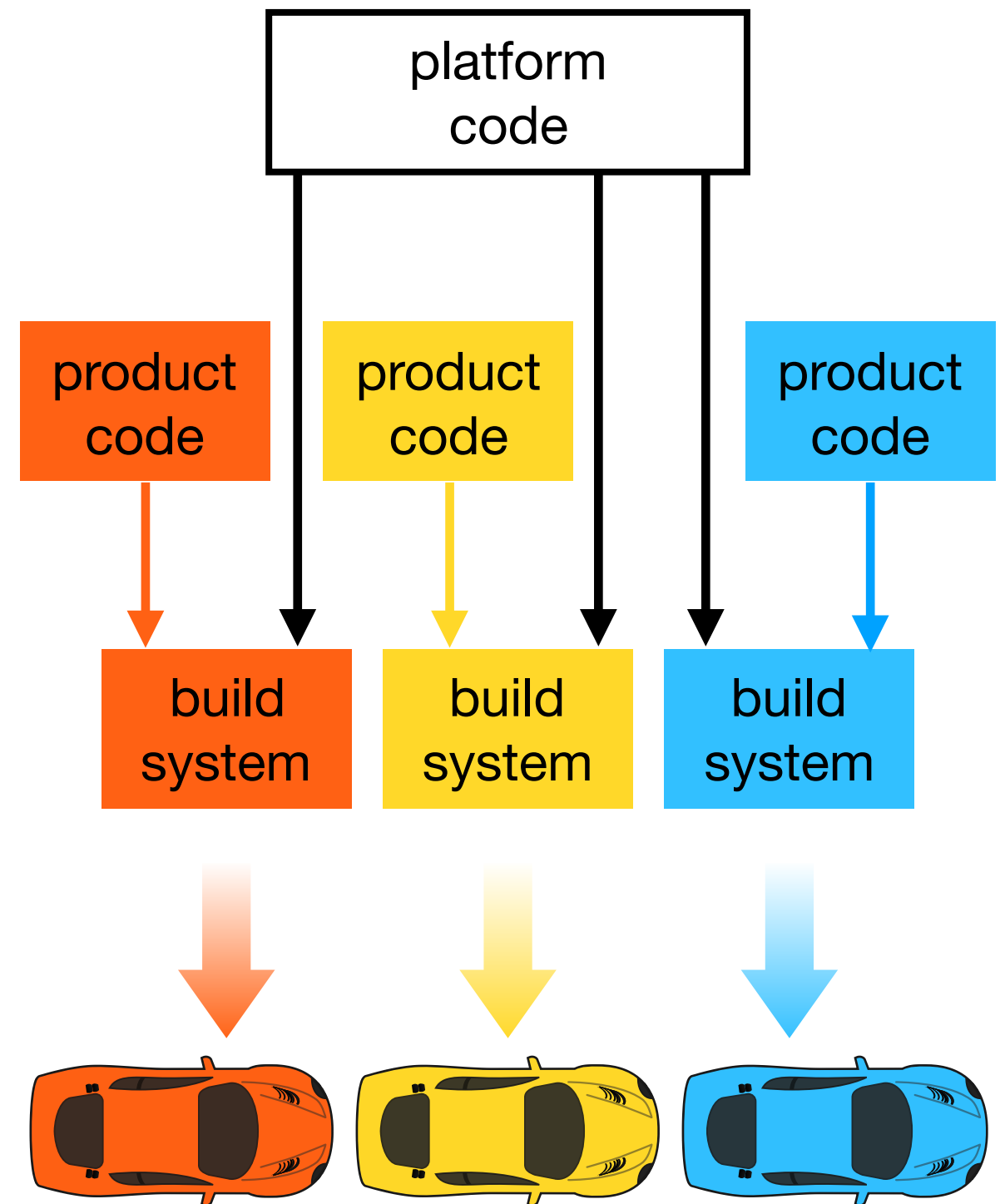
Duplicated Effort



Cloning as Opportunistic Reuse

Dubinsky et al., Exploratory Study of Cloning in Industrial SPLs, CSMR 2013

- + **Easy**, no special tooling required
- + **Quickly** available functionality
- **No** sharing (fixes & features)
- Maintain **yourself** (test, debug, change)
- **Product specific** code grows
- Platform code **diminishes and degrades**



Successful Reuse

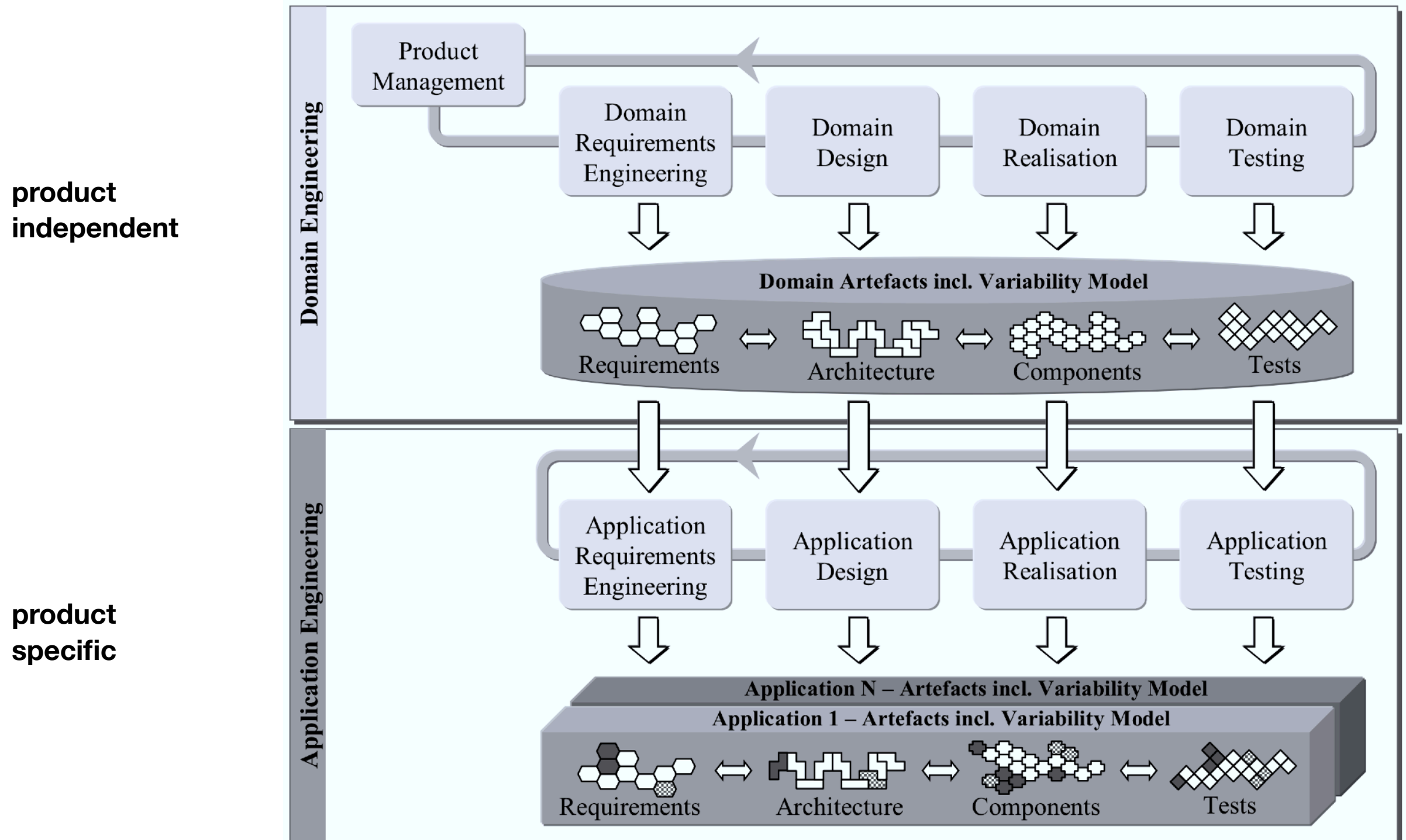
Proactive

Planned

Managed

Domain vs Application Engineering

Pohl et al., Software Product Line Engineering, Springer Science, 2005

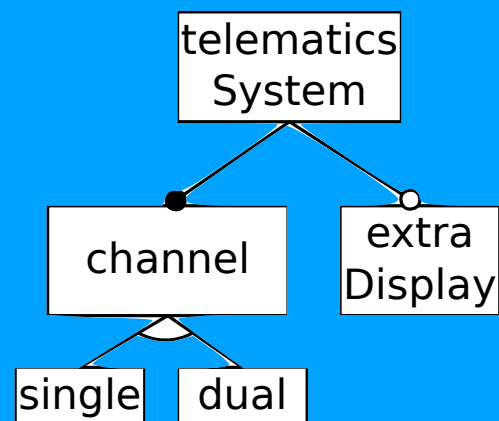


features

variation points

Problem Space

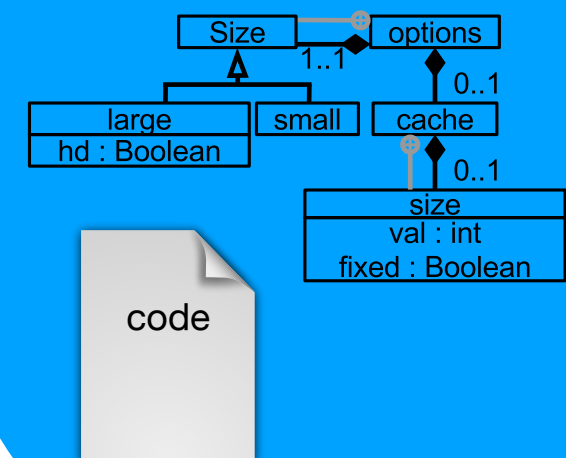
domain-specific
abstractions



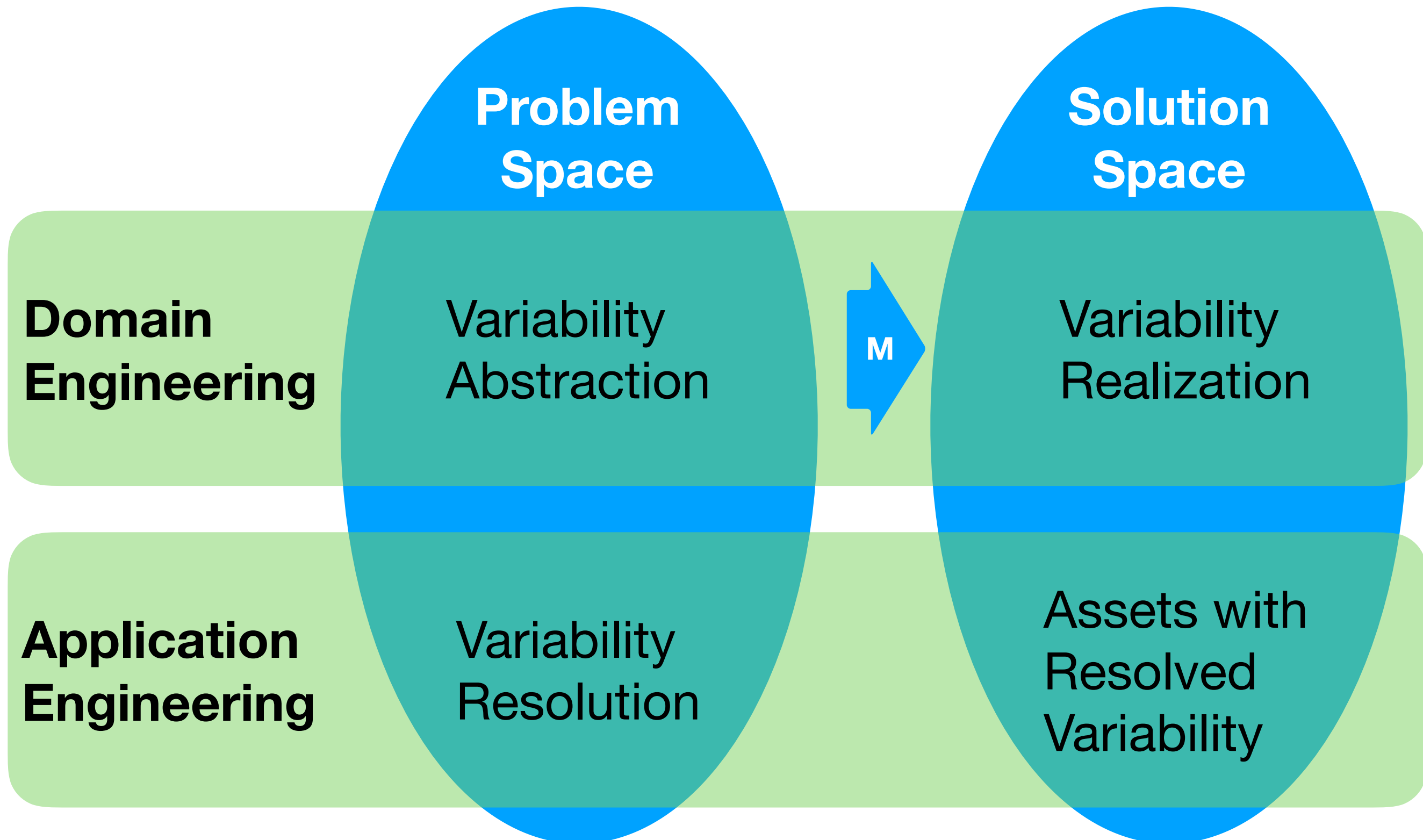
Mapping

Solution Space

implementation
oriented abstractions

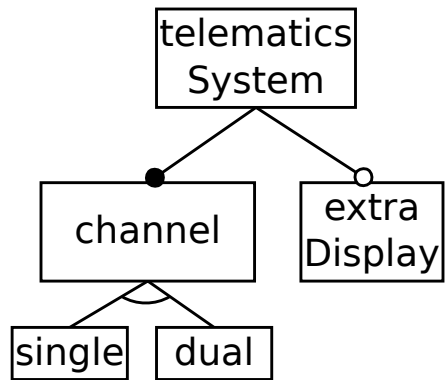


Software Product Line



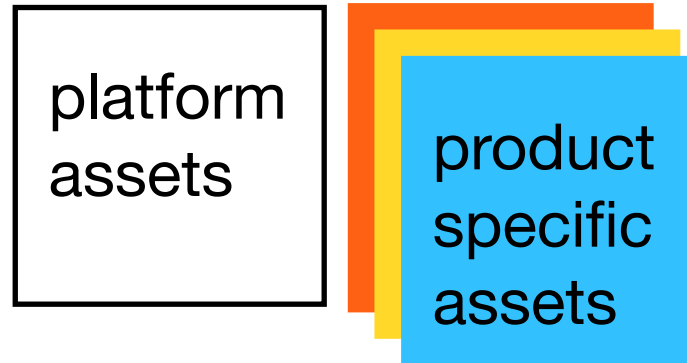
Product Line Architecture Overview

Variability Abstraction



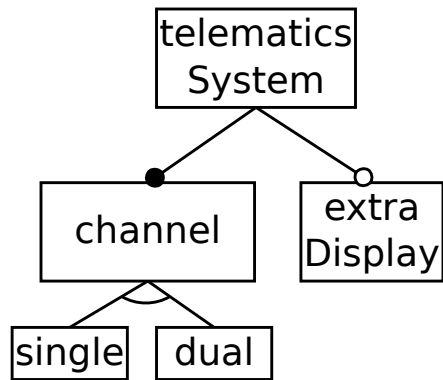
maps to
→

Variability Realization



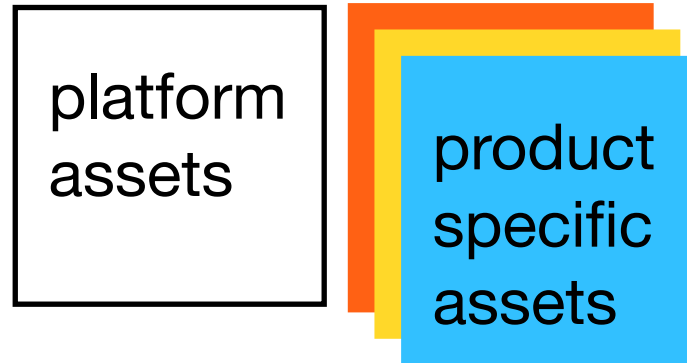
Product Line Architecture Overview

Variability Abstraction



maps to

Variability Realization



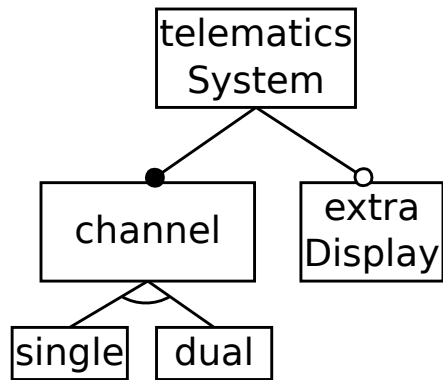
configures

Variability Resolution

!extraDisplay
dual

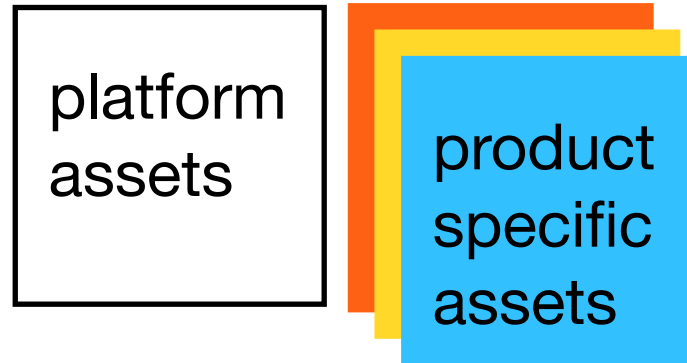
Product Line Architecture Overview

Variability Abstraction



maps to
→

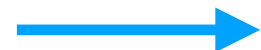
Variability Realization



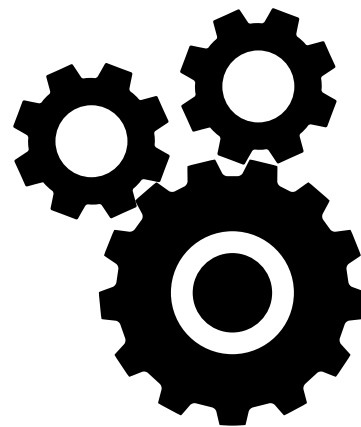
↑
configures

Variability Resolution

!extraDisplay
dual

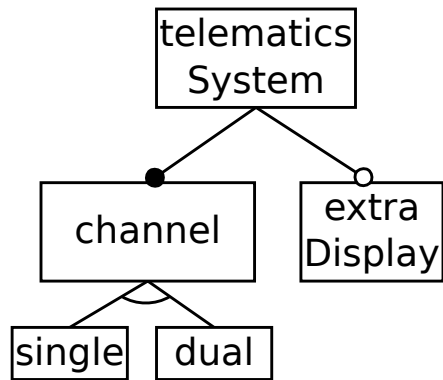


Build System



Product Line Architecture Overview

Variability Abstraction



maps to

Variability Realization

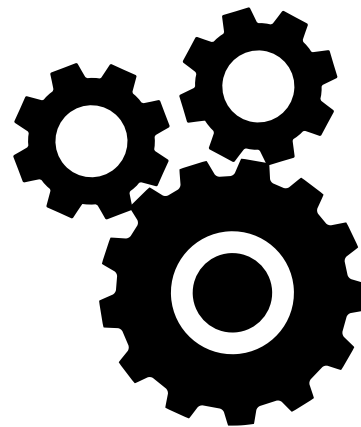


configures

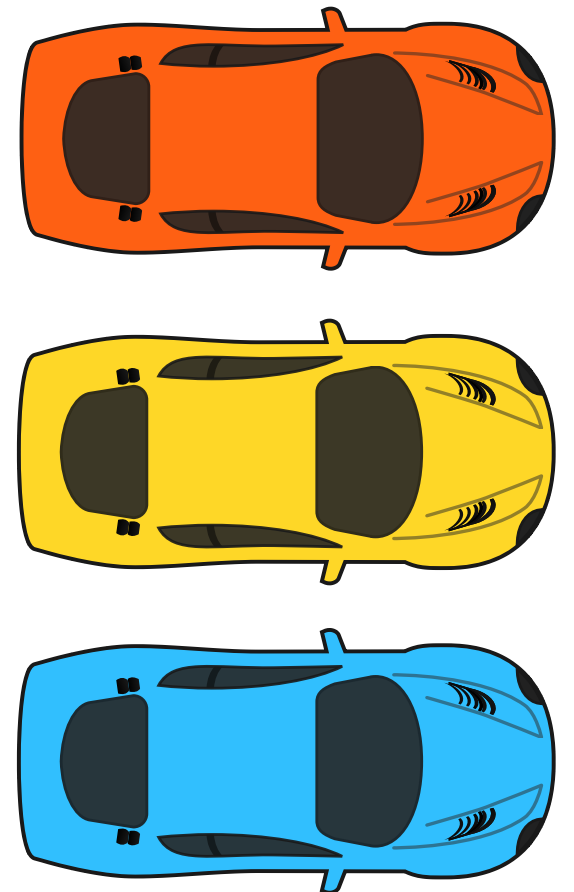
Variability Resolution

!extraDisplay
dual

Build System

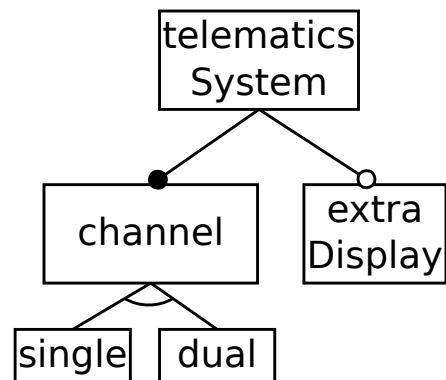


Assets with Resolved Variability



Implementation Technologies

Variability Abstraction



Variability Resolution

!extraDisplay
dual

Variability Realization



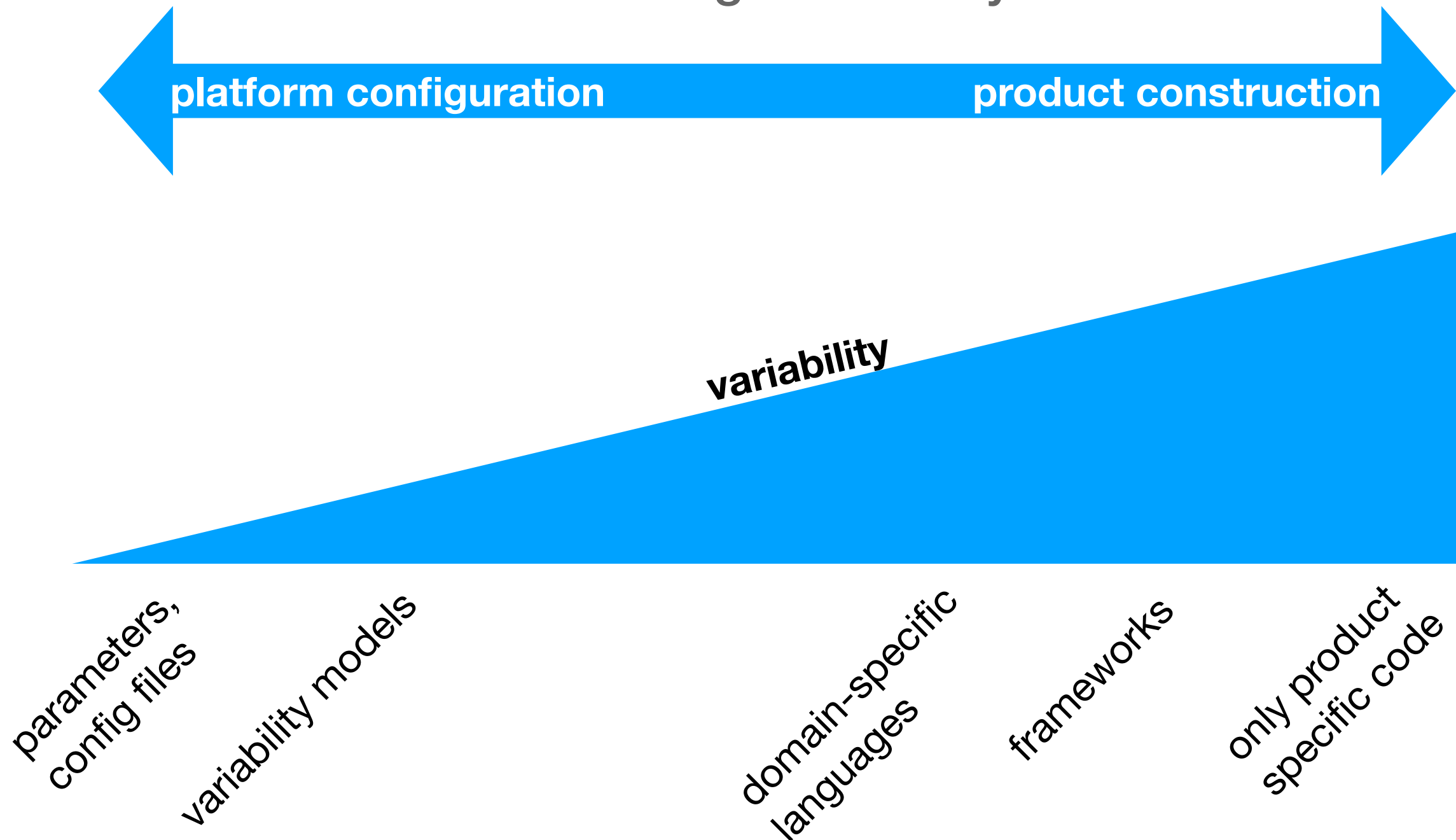
- **Feature models**
- Domain Specific Languages
- *none*
- **Feature model configuration, constraints**
- Domain specific model
- XML, JSON, custom text format, ...
- Code (with variability techniques)
- Code generators
- Model transformers
- Parts may use DSLs

Spectrum of Variability Architectures

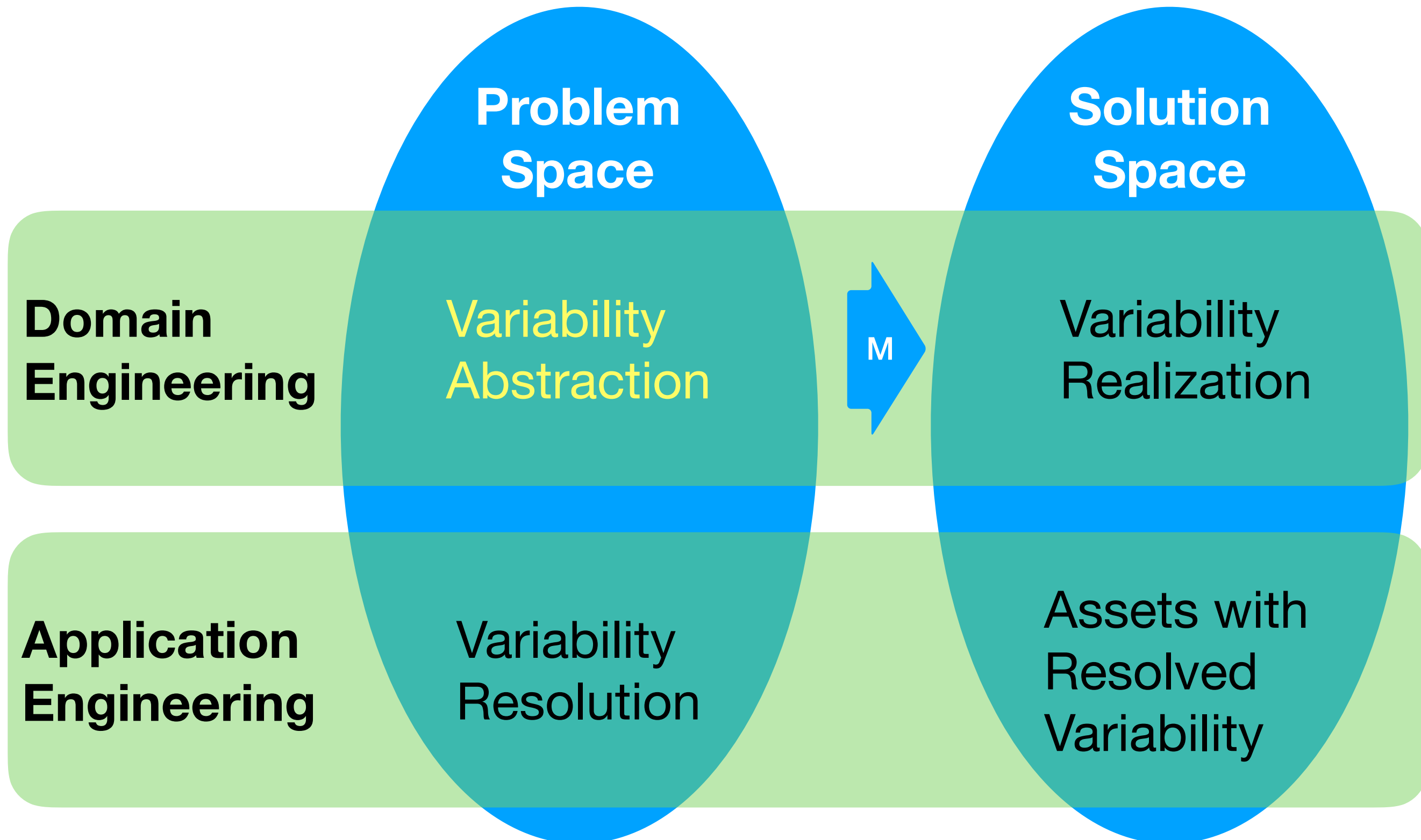
Stay as Close to the Left as Possible

Exploit Commonality

Manage Variability

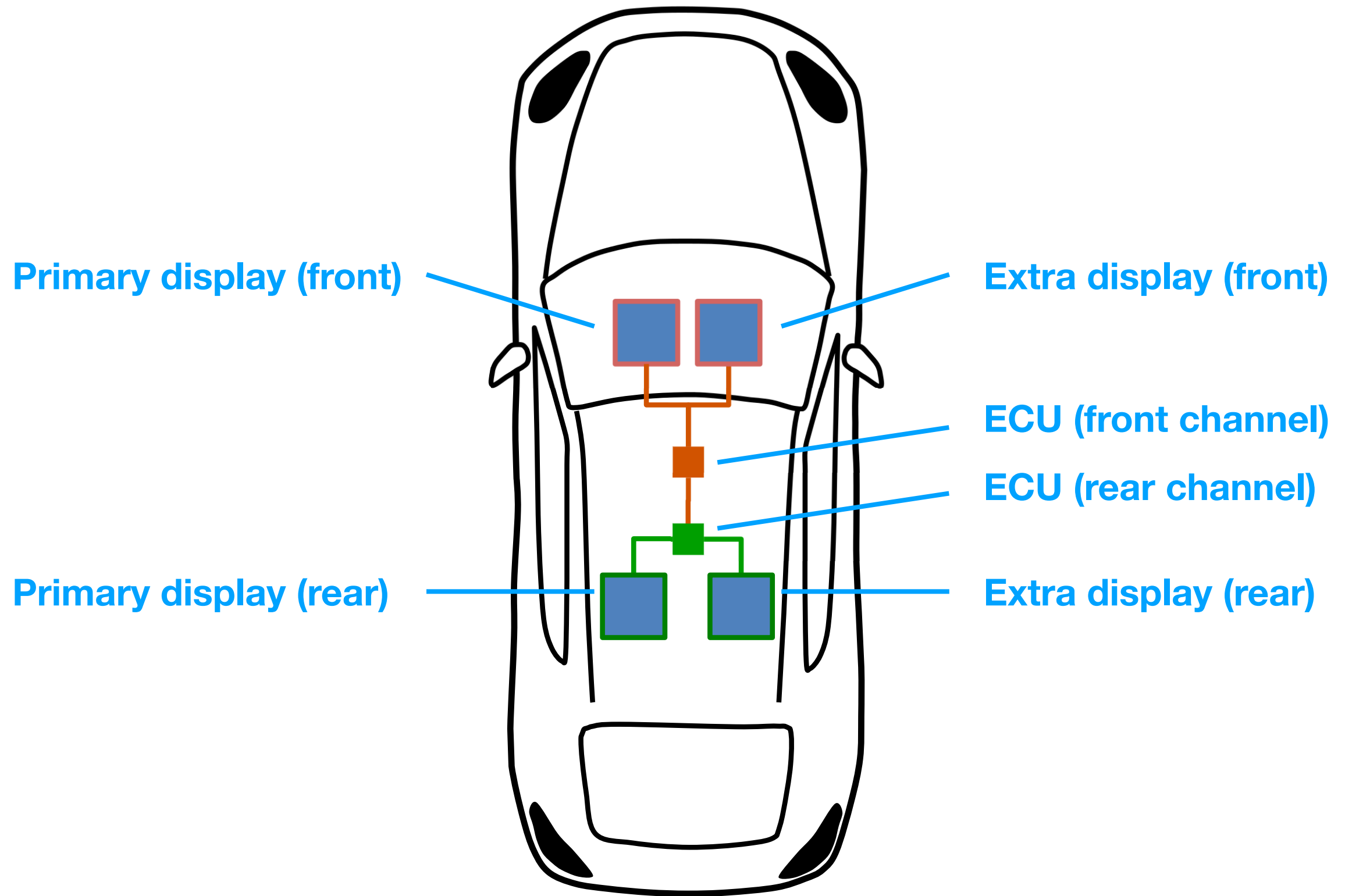


Software Product Line

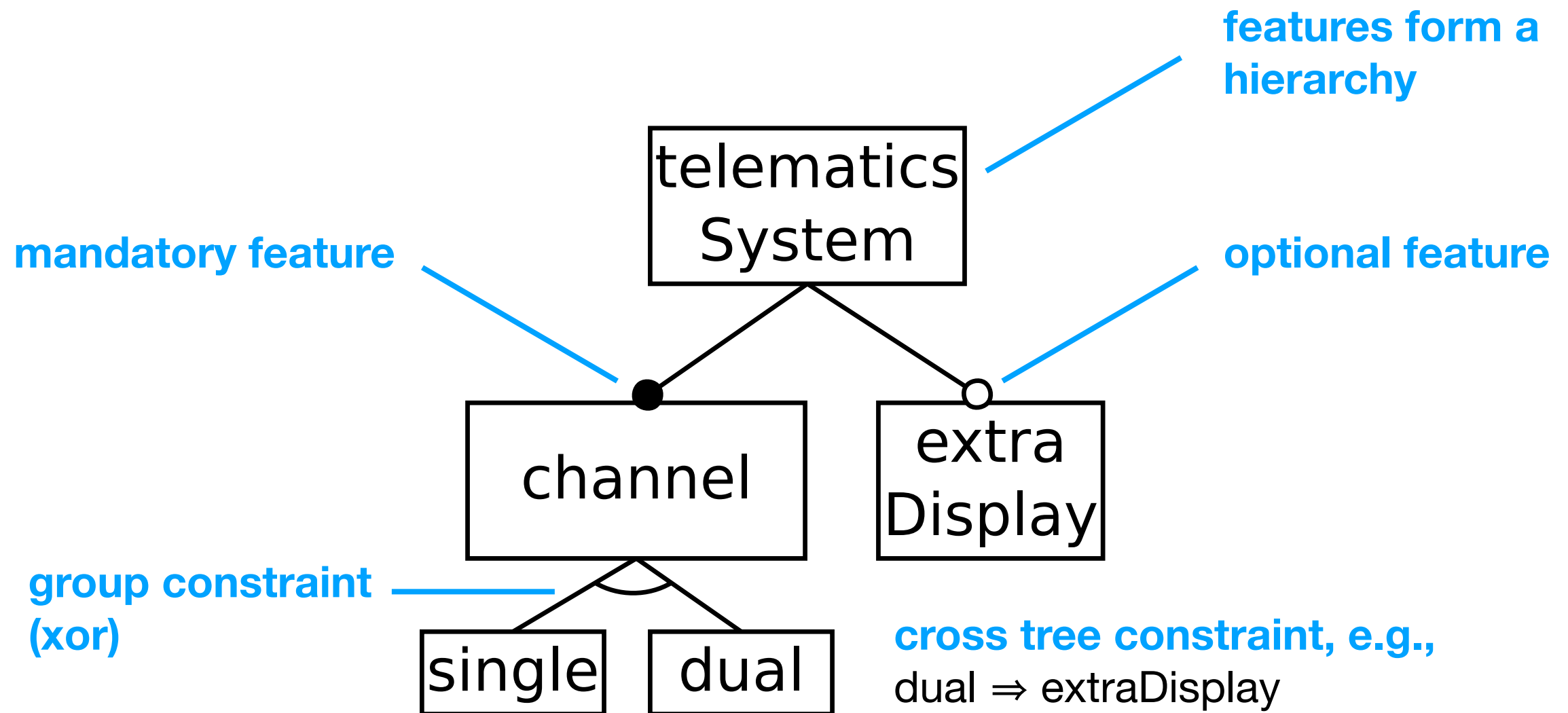


Example: Telematics System



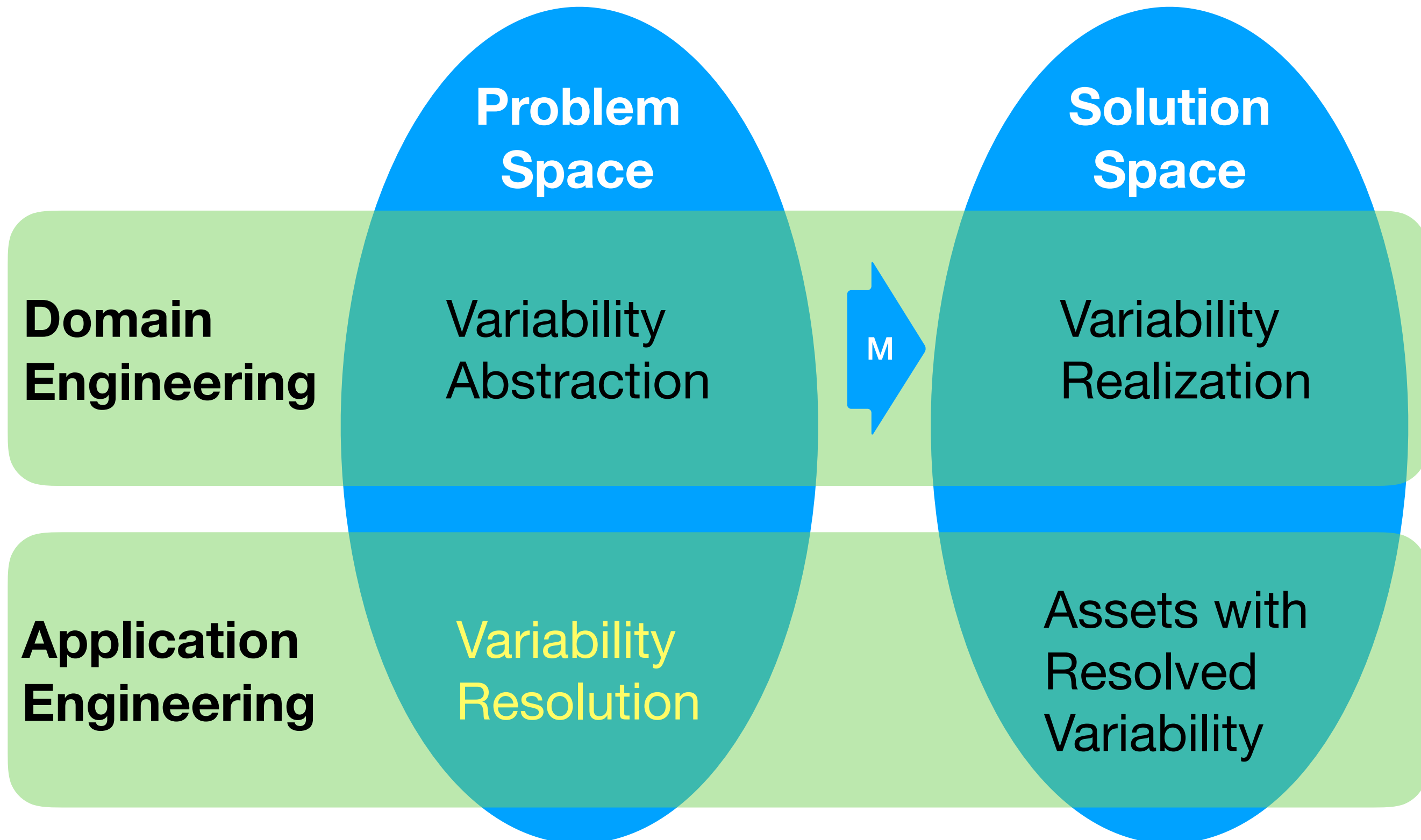


Feature Model

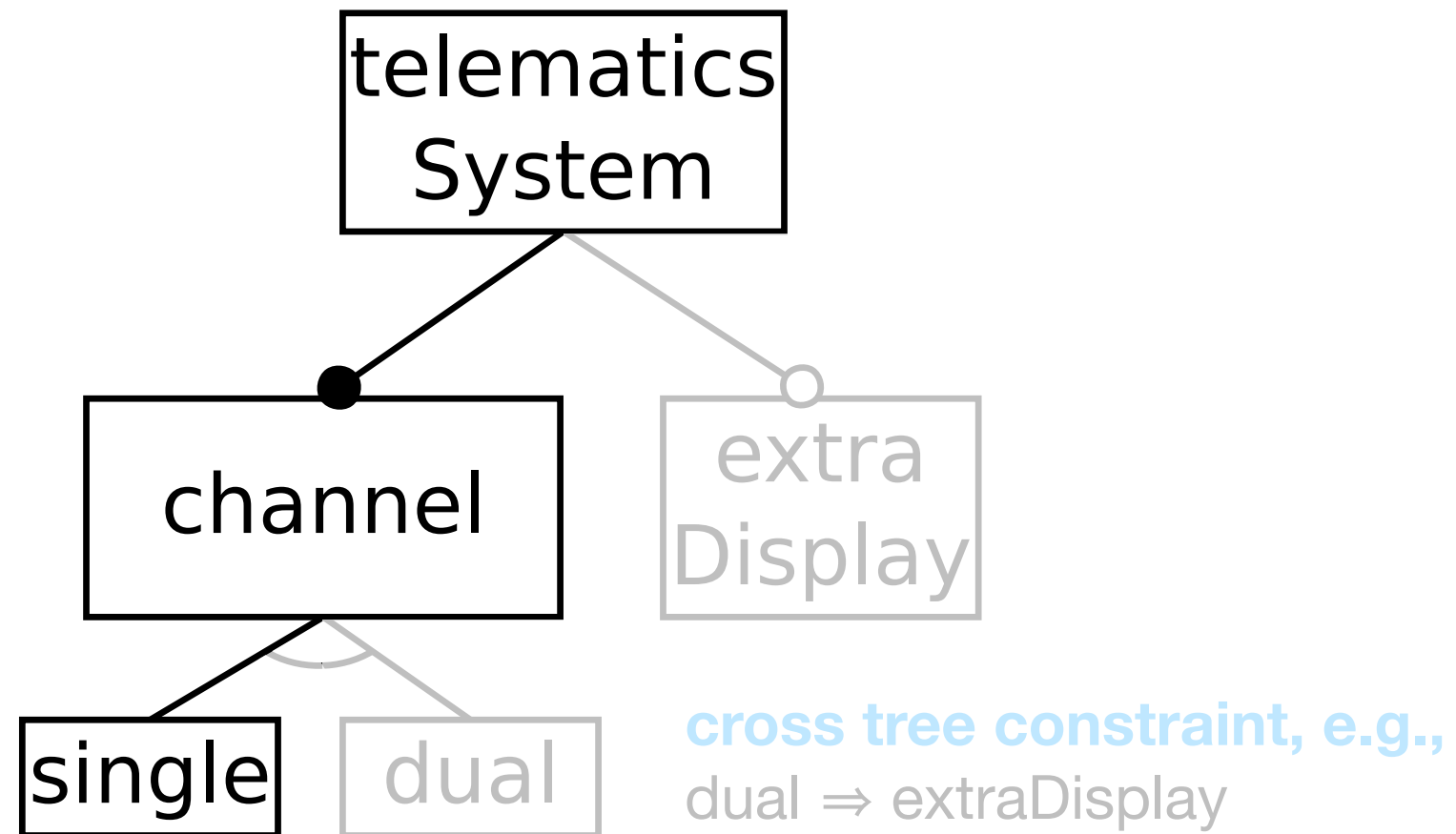


single kind of relationship: *subfeature*
meaning: *implication*

Software Product Line



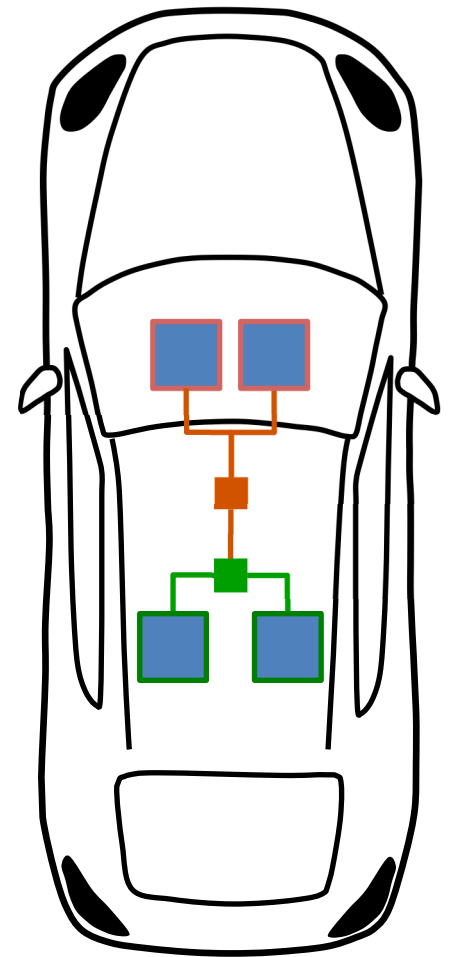
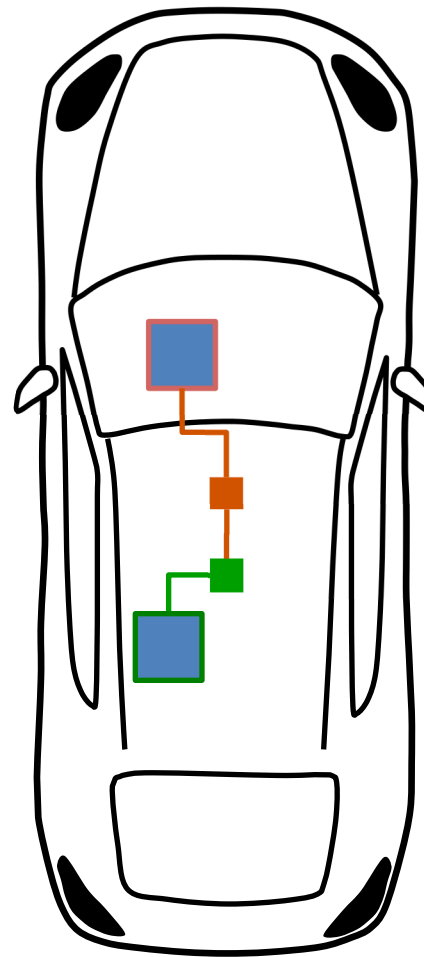
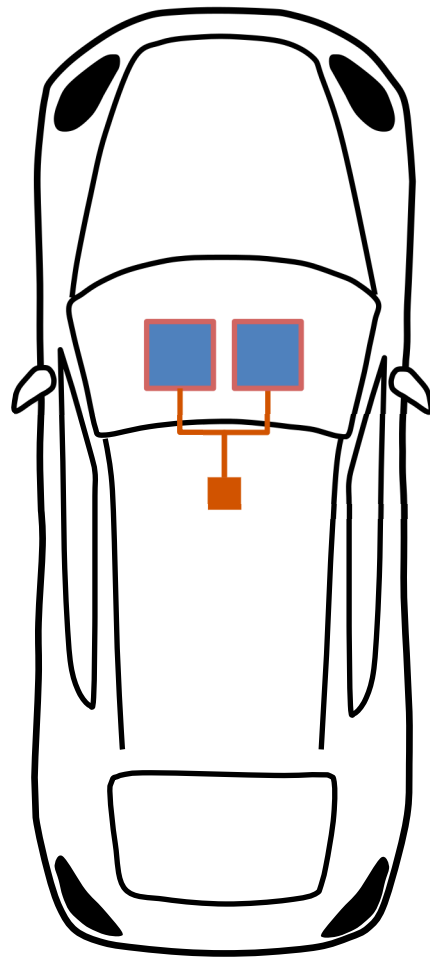
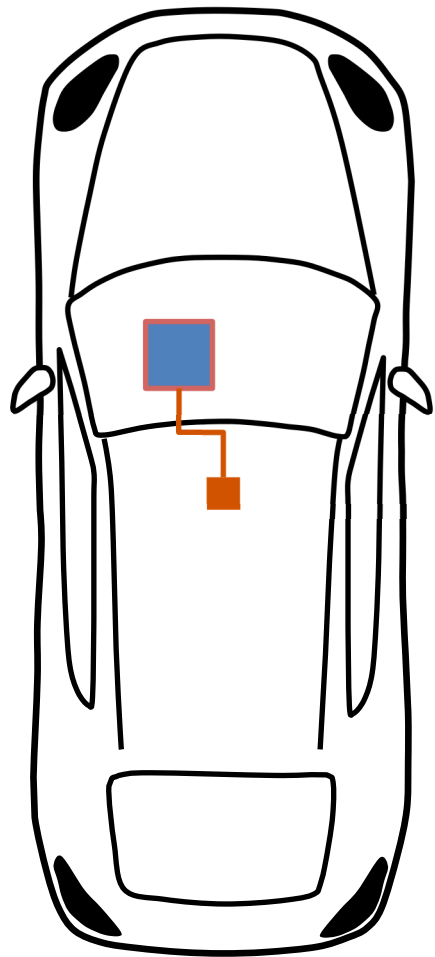
Feature Configuration



{ telematicsSystem,
channel, **single** }

Configuration Semantics of an FM

Set of Configurations



{ telematicsSystem,
channel, **single** }

{ telematicsSystem,
channel, **single**,
extraDisplay }

{ telematicsSystem,
channel, **dual** }

{ telematicsSystem,
channel, **dual**,
extraDisplay }

Feature Modeling and FODA

- FODA succeeds for its **simplicity**
- Probably best intro in Czarnecki's *Generative Programming* (Ch. 4)
- 4700+ citations, **never formally published**



Scholar

About 3,060,000 results (0.10 sec)

Articles

Feature-oriented domain analysis (FODA) feasibility study

[KC Kang, SG Cohen, JA Hess, WE Novak, AS Peterson - 1990 - dtic.mil](#)

Case law

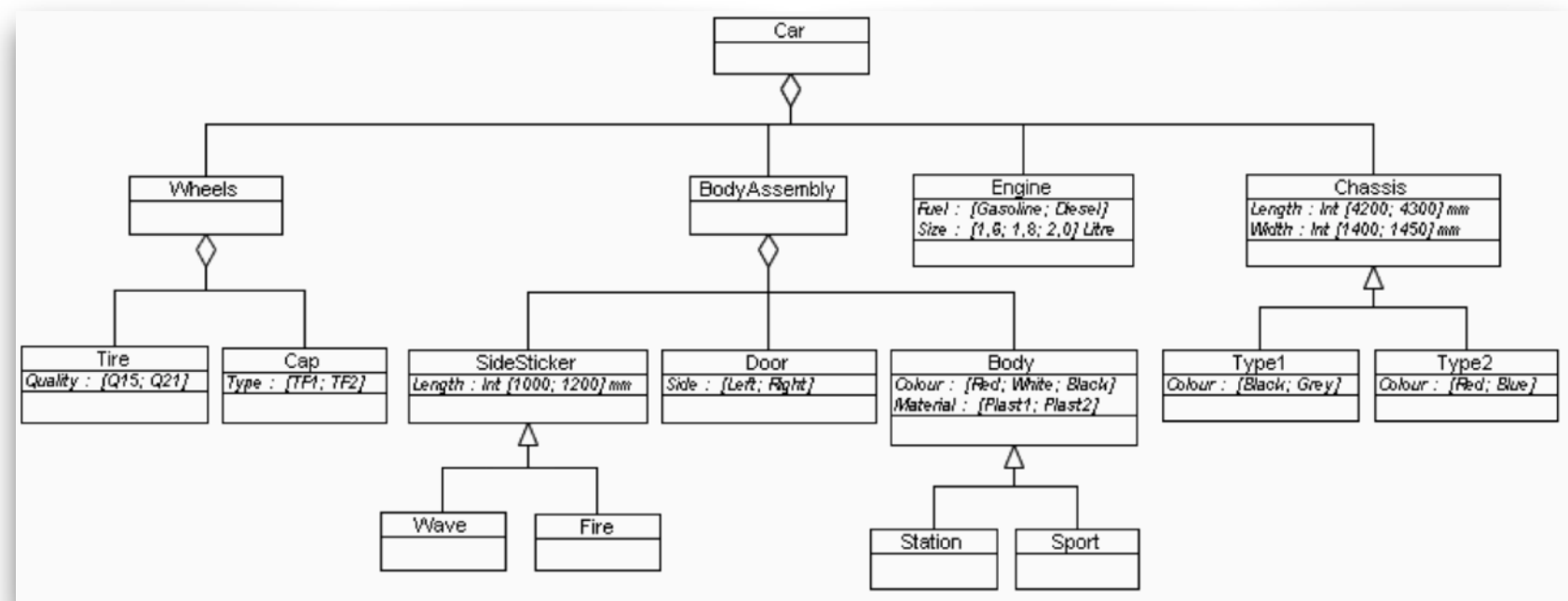
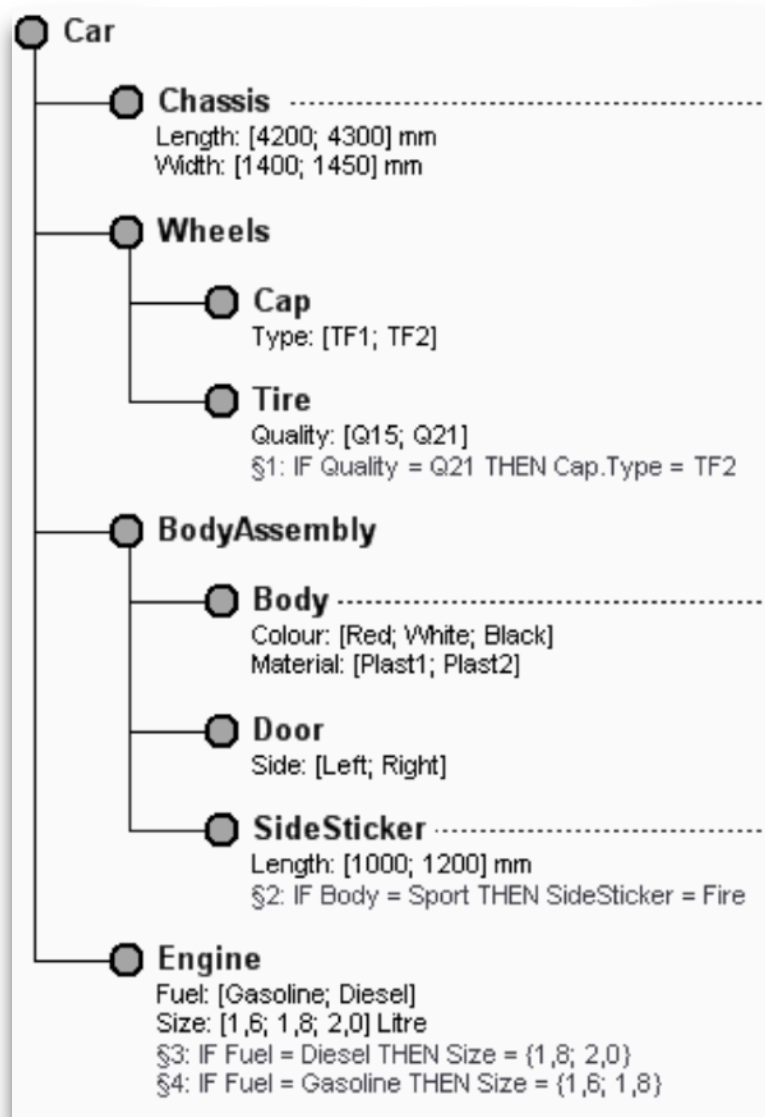
Abstract: Successful Software reuse requires the systematic discovery and exploitation of commonality across related software systems. By examining related software systems and the underlying theory of the class of systems they represent, **domain analysis** can provide a

My library

[Cited by 4704](#) [Related articles](#) [All 14 versions](#) [Cite](#) [Save](#)

Feature Models vs Class Models

A Feature Model in *Product Variant Master* Notation (Hvam)



Haug et al., Creating a documentation system to support the development and maintenance of product configuration systems, WSEAS 2007

Feature Models vs Class Models

Concepts	Few and simple: feature, subfeature, group, constraint	Many and complex: class, generalization, composition, association, redefinition, refinement, property, multiplicity, package, data type, primitive type, enumeration, ...
Use	Variation of <i>user-relevant</i> characteristics of product variants	Concepts representing more detailed aspects of products; <i>product line architectures</i>
Semantics	Configuration - selections from <i>predefined choices</i> within a fixed tree structure	Instantiation - making <i>new</i> structures that conform to predefined types, and <i>connecting</i> them via links

How to Build Feature Models?

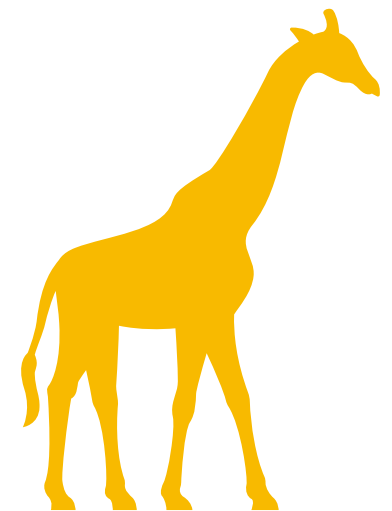
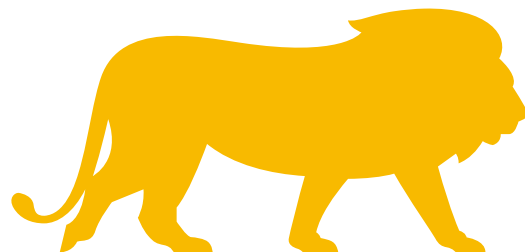
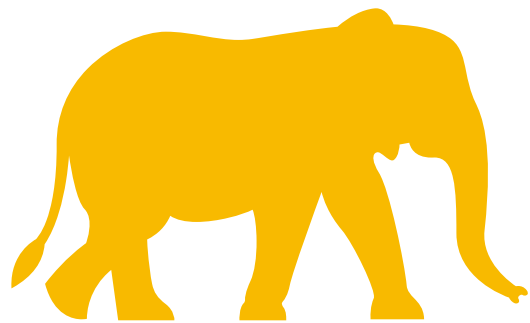
Bottom Up - Incremental Adoption

- Identify **cloned** code/functionality
- Find the **patches** that describe differences
- Diffs → **variation points**
- Aggregate variation points into hierarchical **features**

Jepsen et al., Minimally Invasive Migration to Software Product Lines, SPLC 2007

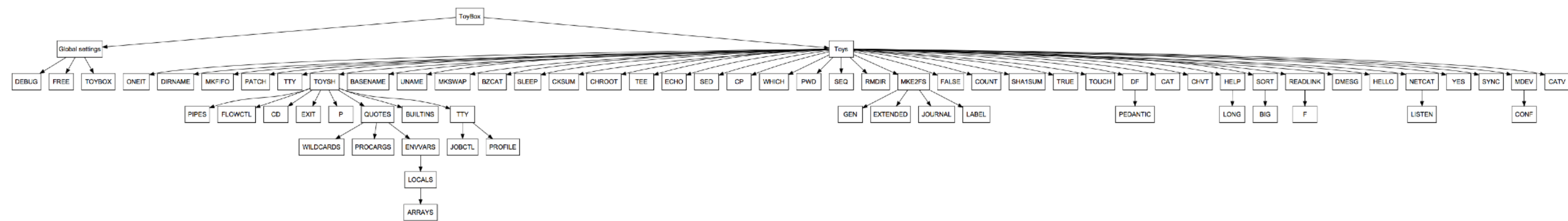
Berger et al., A survey of variability modeling in industrial practice, VAMOS 2013

Variability Modeling in the Wild



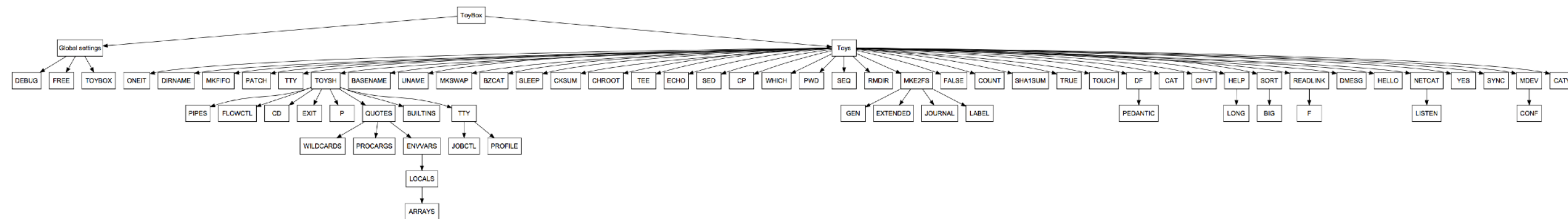
Healthy Wild Variability Model Club

ToyBox Project, 71 Features



Healthy Wild Variability Model Club

ToyBox Project, 71 Features



The Linux Kernel has 6-12k features, depending on how you count.

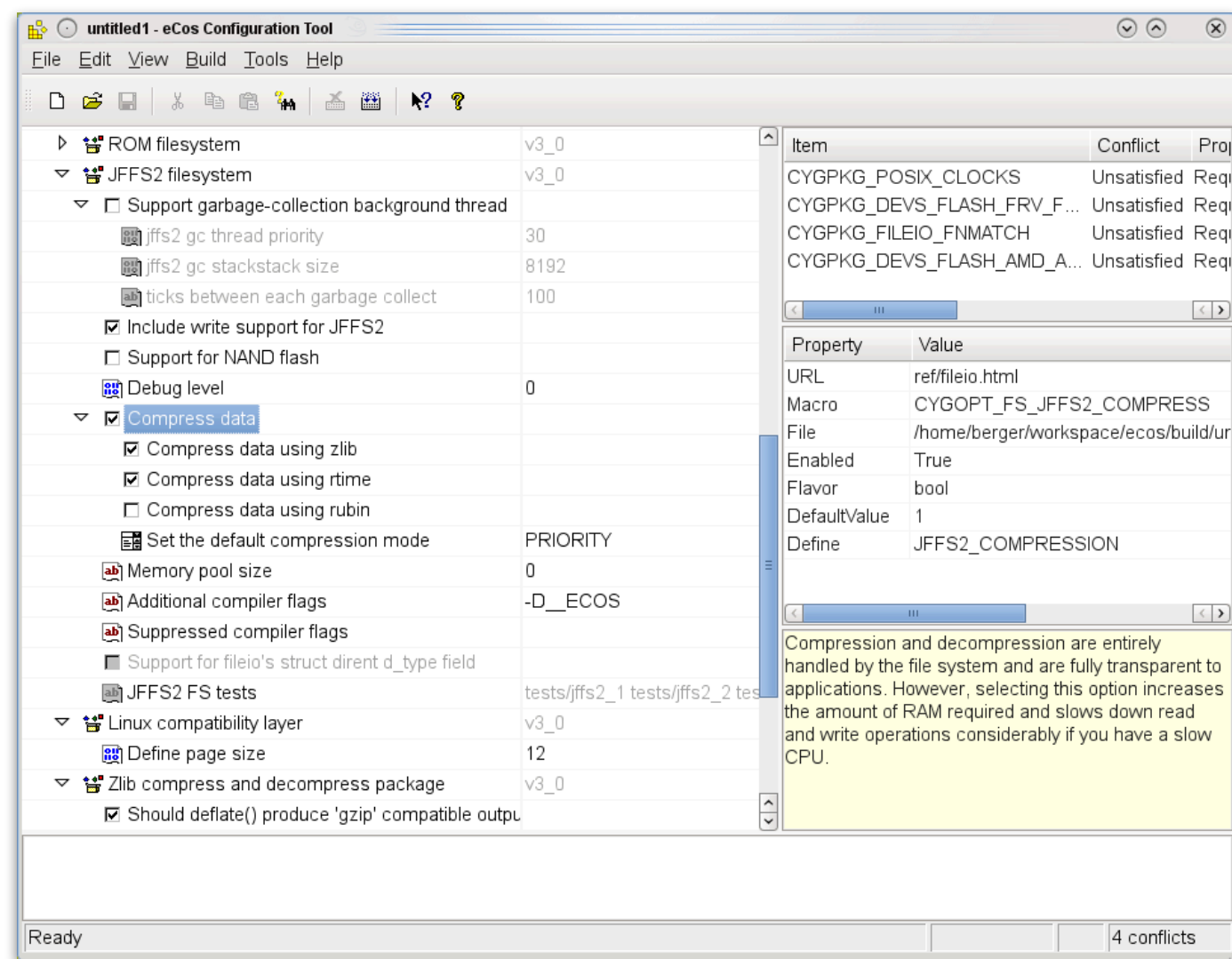
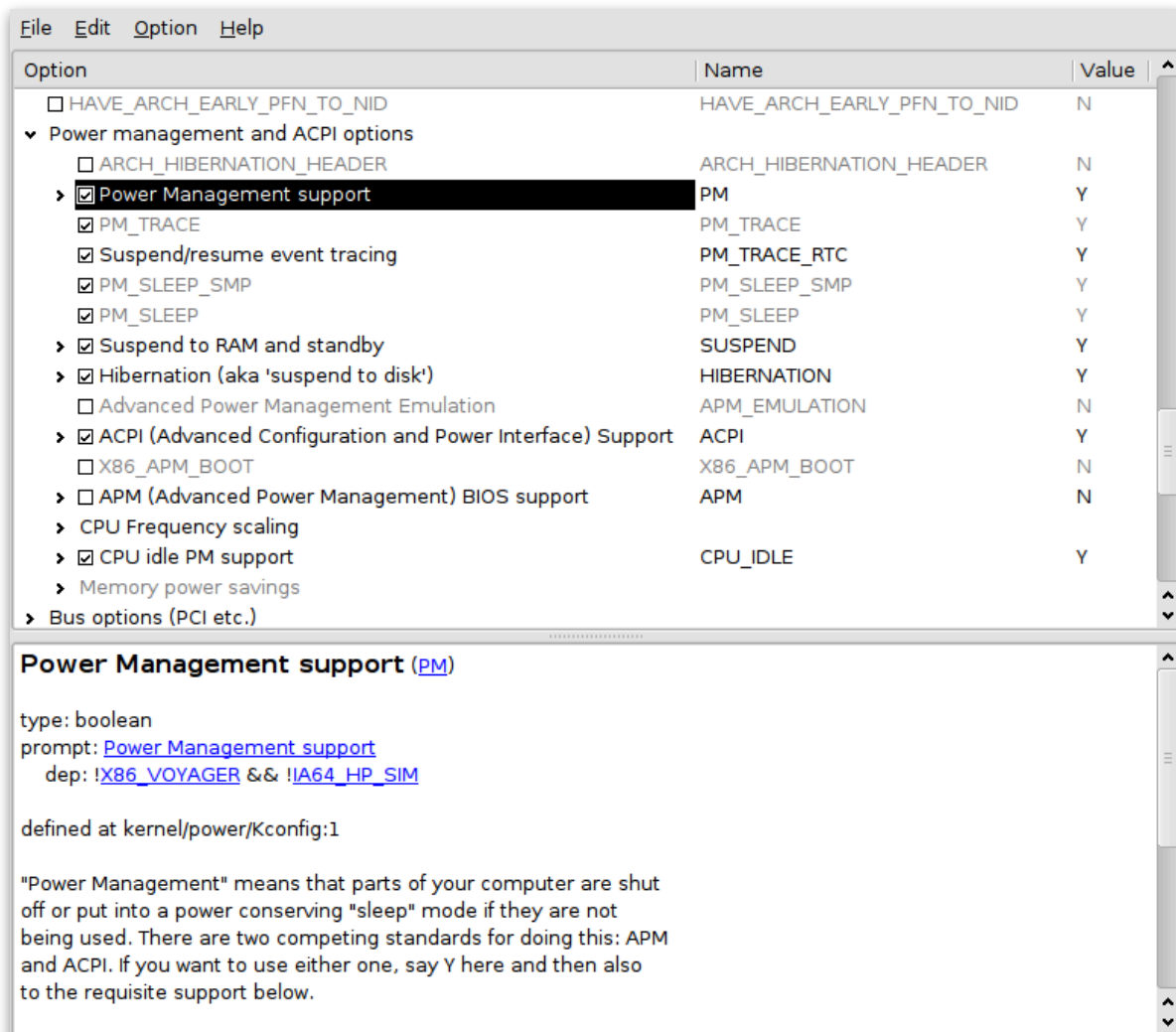
Max depth: 8. Most leaves are at 4! **Shallow**

↓ this is the Linux Kernel model fit to the slide width ↓

Berger et al., A Study of Variability Models and Languages in the Systems Software Domain, TSE 2013

KConfig & CDL

FM with Attributes, Defaults, Constraint Propagation, UI, ...



KConfig & CDL

Textual Variability Models

File Edit Option Help

Option	Name	Value
<input type="checkbox"/> HAVE_ARCH_EARLY_PFN_TO_NID	HAVE_ARCH_EARLY_PFN_TO_NID	N
▼ Power management and ACPI options		
<input type="checkbox"/> ARCH_HIBERNATION_HEADER	ARCH_HIBERNATION_HEADER	N
<input checked="" type="checkbox"/> Power Management support	PM	Y
<input checked="" type="checkbox"/> PM_TRACE	PM_TRACE	Y
<input checked="" type="checkbox"/> Suspend/resume event tracing	PM_TRACE_RTC	Y
<input checked="" type="checkbox"/> PM_SLEEP_SMP		
<input checked="" type="checkbox"/> PM_SLEEP		
▼ Suspend to RAM and standby		
▼ Hibernation (aka 'suspend to disk')		
<input type="checkbox"/> Advanced Power Management Emulation		
▼ ACPI (Advanced Configuration and Power Interface)		
<input type="checkbox"/> X86_APM_BOOT		
<input type="checkbox"/> APM (Advanced Power Management) BIOS support		
▼ CPU Frequency scaling		
▼ CPU idle PM support		
▼ Memory power savings		
▼ Bus options (PCI etc.)		

Power Management support (PM)

type: boolean
prompt: [Power Management support](#)
dep: [!X86_VOYAGER](#) && [!A64_HP_SIM](#)

defined at kernel/power/Kconfig:1

"Power Management" means that parts of your computer can be turned off or put into a power conserving "sleep" mode if they are not being used. There are two competing standards for doing this, ACPI and APM. If you want to use either one, say Y here and then to the requisite support below.

```

k-1 menuconfig MISC_FILESYSTEMS
k-2   bool "Miscellaneous filesystems"
k-3
k-4   if MISC_FILESYSTEMS
k-5
k-6     config JFFS2_FS
k-7       tristate "Journalling Flash File System" if MTD
k-8       select CRC32 if MTD
k-9
k-10
k-11
k-12
k-13     config JFFS2_FS_DEBUG
k-14       int "JFFS2 Debug level (0=quiet, 2=noisy)"
k-15       depends on JFFS2_FS
k-16       default 0
k-17       range 0 2
k-18       --- help ---
k-19         Debug verbosity of ...
k-20
k-21
k-22     config JFFS2_FS_WRITEBUFFER
k-23       bool
k-24       depends on JFFS2_FS
k-25       default HAS_IOMEM
k-26
k-27
k-28     config JFFS2_COMPRESS
k-29       bool "Advanced compression options for JFFS2"
k-30       depends on JFFS2_FS
k-31
k-32     config JFFS2_ZLIB
k-33       bool "Compress w/zlib..." if JFFS2_COMPRESS
k-34       depends on JFFS2_FS
k-35       select ZLIB_INFLATE
k-36       default y
k-37
k-38     choice
k-39       prompt "Default compression" if JFFS2_COMPRESS
k-40       default JFFS2_CMODE_PRIORITY
k-41       depends on JFFS2_FS
k-42       config JFFS2_CMODE_NONE
k-43         bool "no compression"
k-44       config JFFS2_CMODE_PRIORITY
k-45         bool "priority"
k-46       config JFFS2_CMODE_SIZE
k-47         bool "size (EXPERIMENTAL)"
k-48     endchoice
k-49   endif

```

untitled1 - eCos Configuration Tool

File Edit View Build Tools Help

Item	Conflict	Prop
CYGPKG_POSIX_CLOCKS	Unsatisfied Requi	
CYGPKG_DEVS_FLASH_FRV_F...	Unsatisfied Requi	
CYGPKG_FILEIO_FNMATCH	Unsatisfied Requi	
CYGPKG_DEVS_FLASH_AMD_A...	Unsatisfied Requi	

Property Value

Property	Value
URL	ref/fileio.html
Macro	CYGOPT_FS_JFFS2_COMPRESS
File	/home/berger/workspace/ecos/build/ur
Enabled	True
Flavor	bool
DefaultValue	1
Define	JFFS2_COMPRESSION

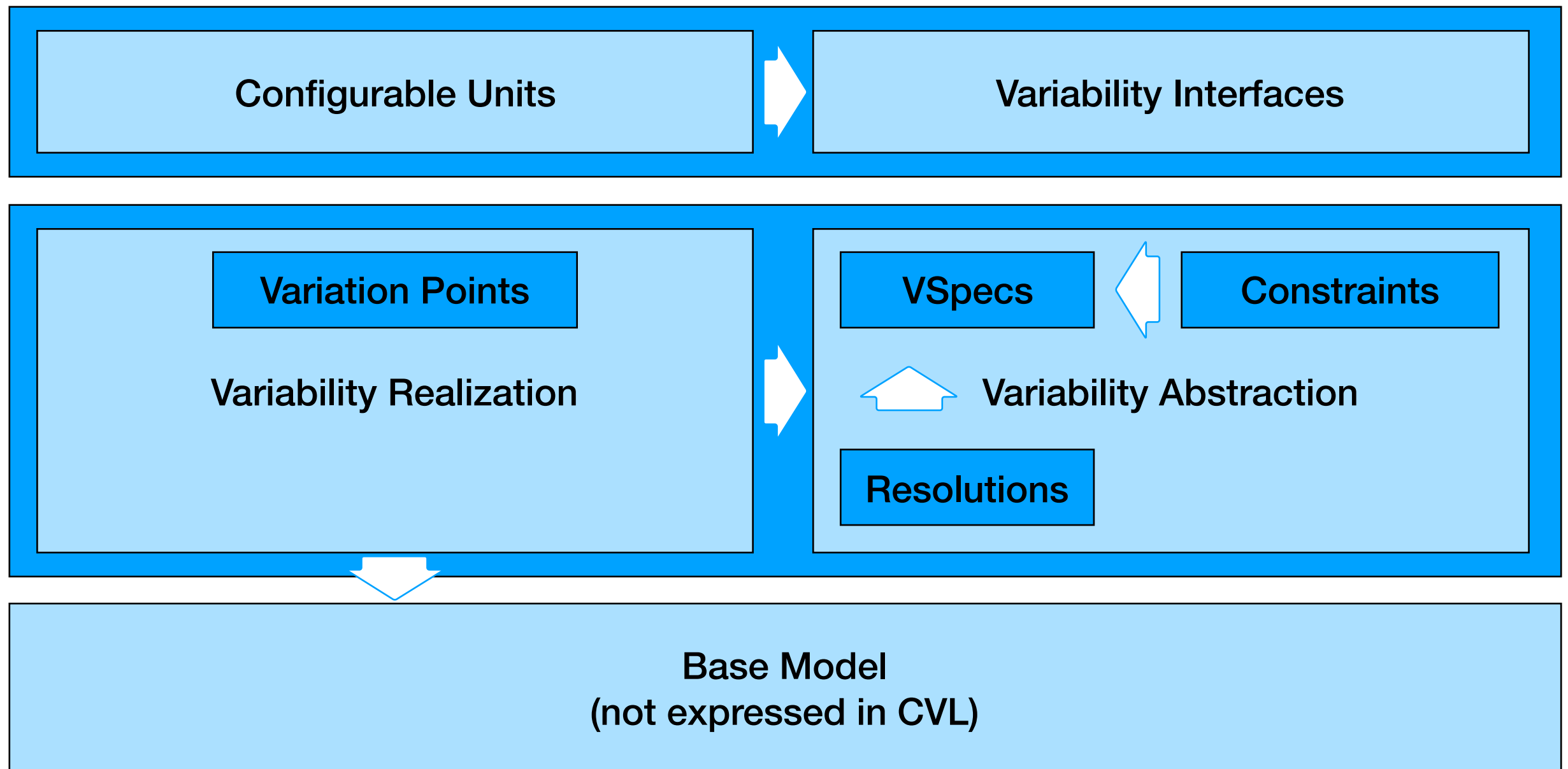
Compression and decompression are entirely handled by the file system and are fully transparent to applications. However, selecting this option increases the amount of RAM required and slows down read and write operations considerably if you have a slow CPU.

4 conflicts

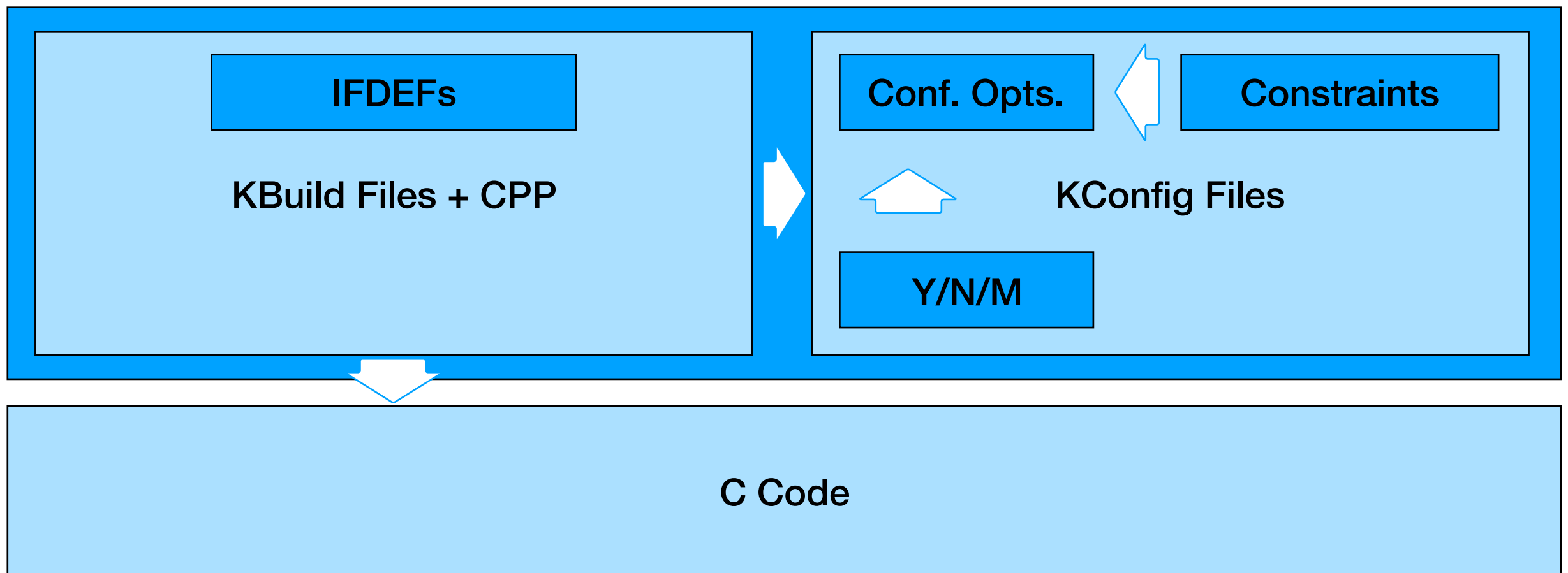


Common Variability Language (CVL)

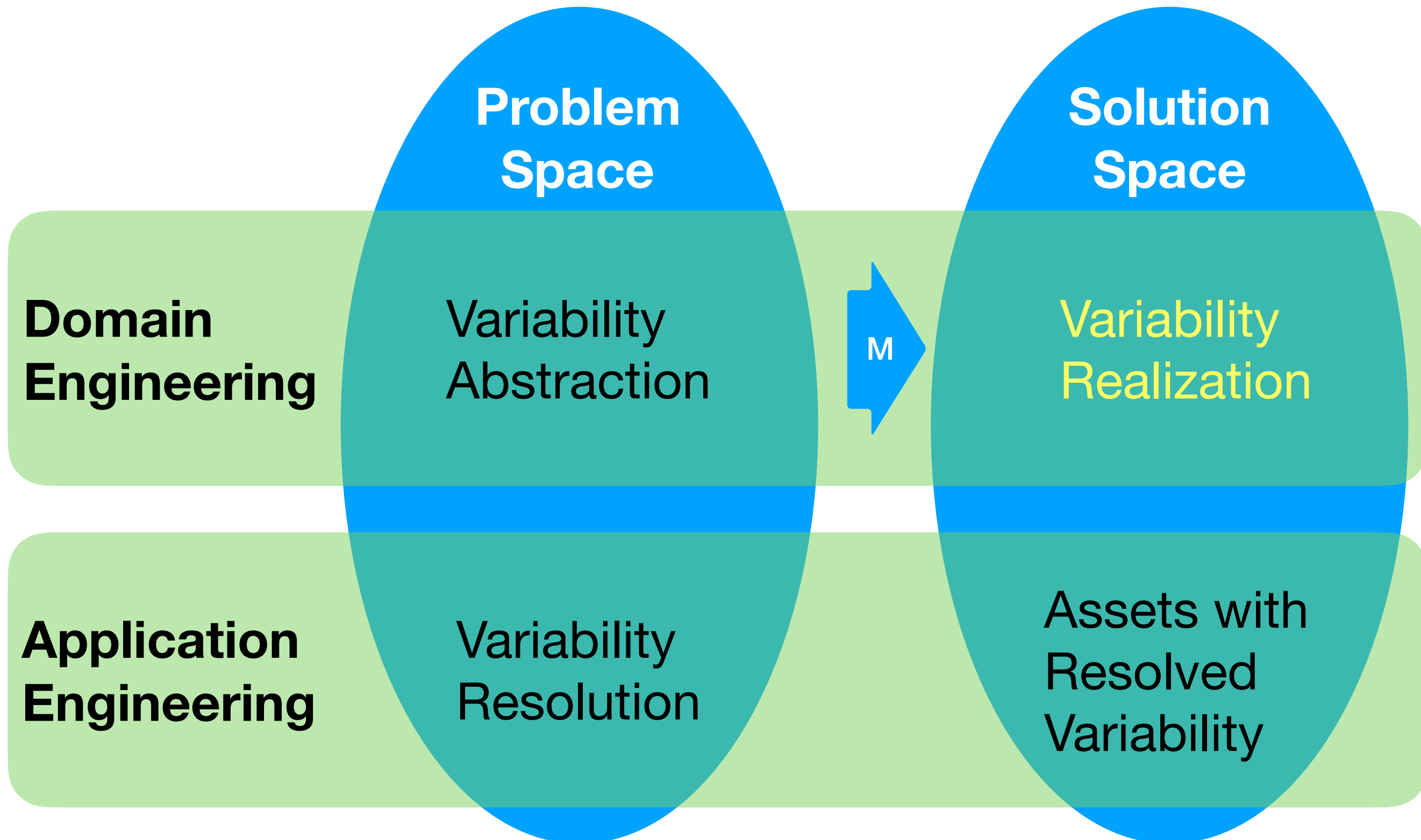
IBM et al., Proposal for CVL Revised Submission, 2012



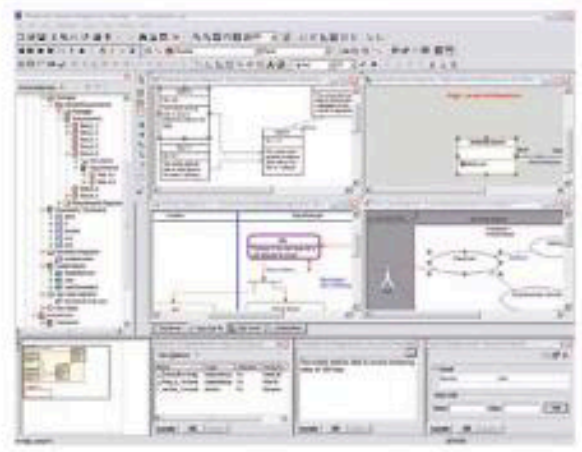
CVL Architecture for Linux Junkies



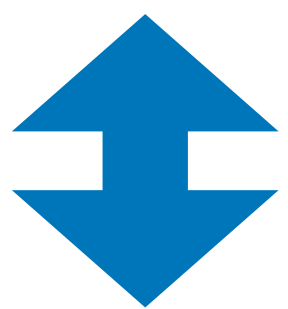
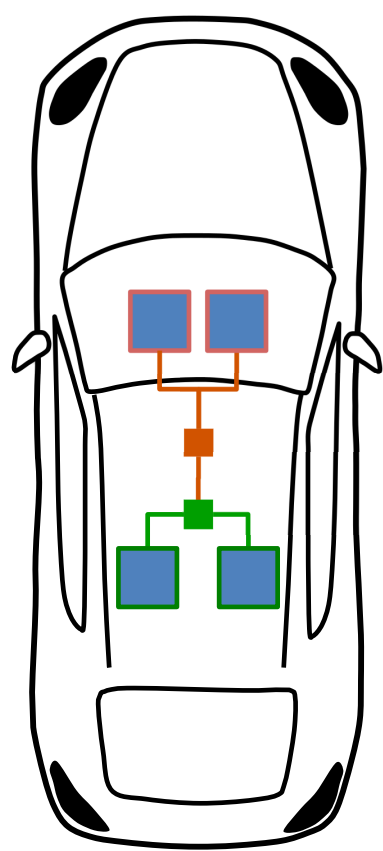
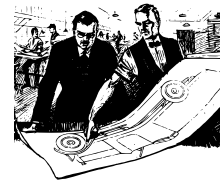
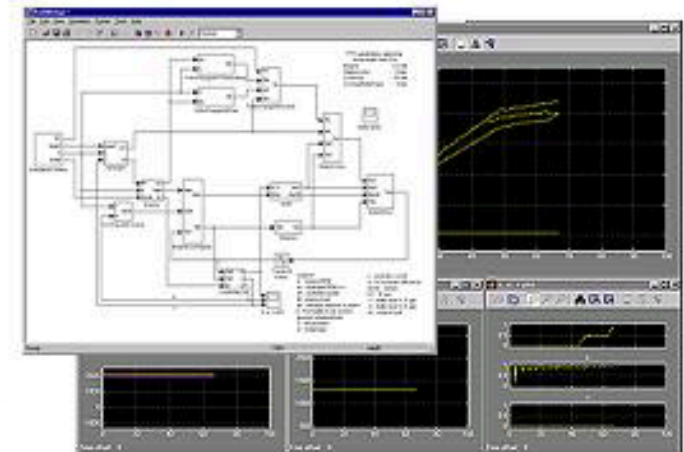
Software Product Line



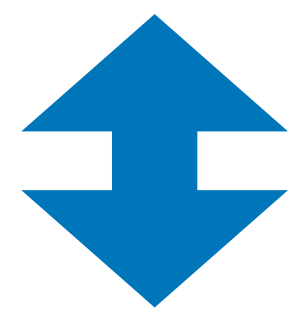
UML Models



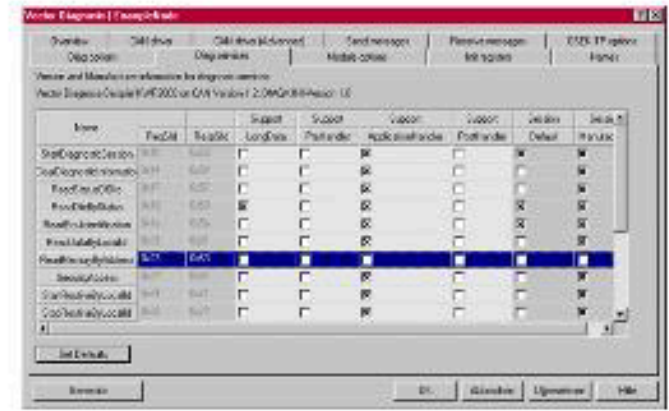
Hybrid Models



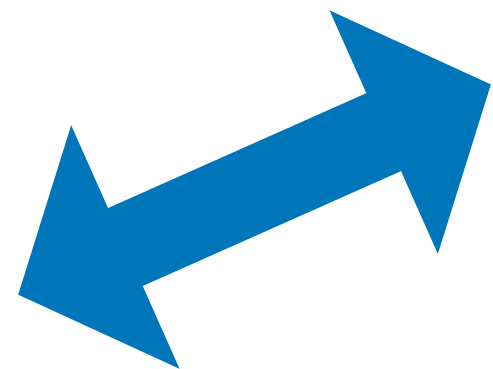
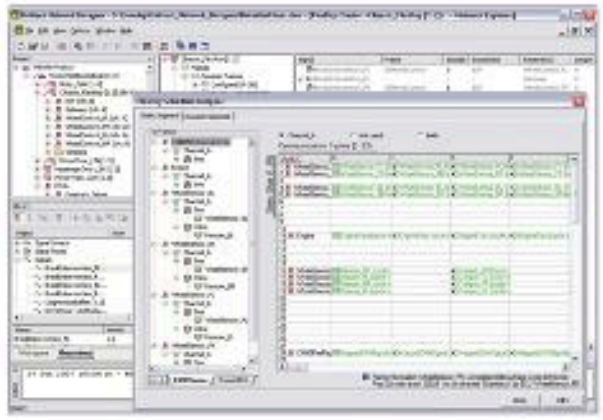
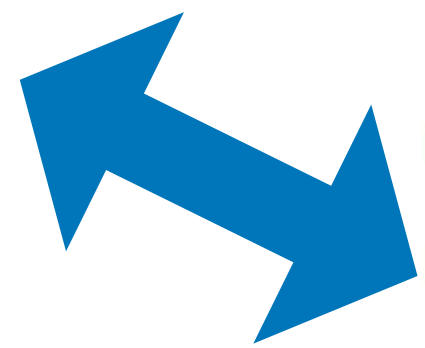
Calibrations



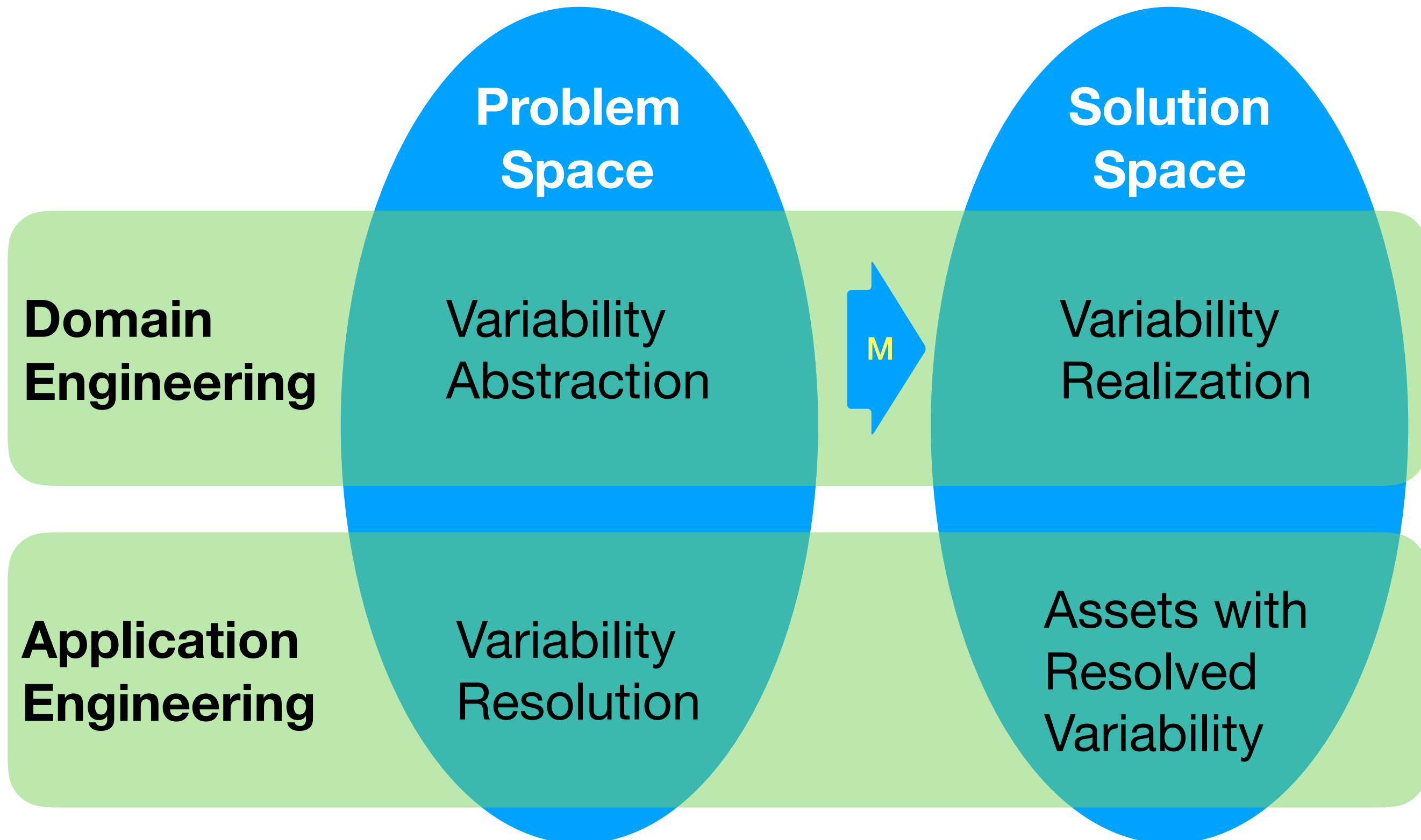
OS Generation



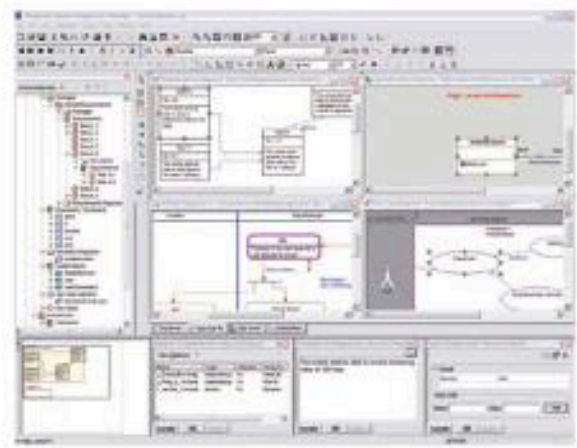
HW/SW Mapping



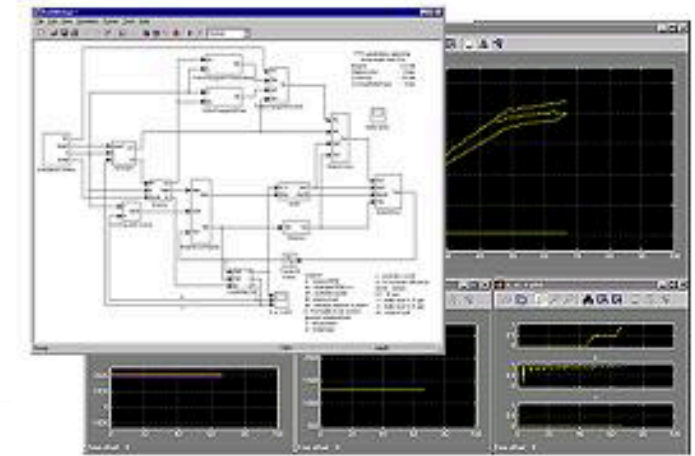
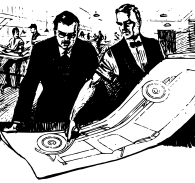
Software Product Line



UML Models

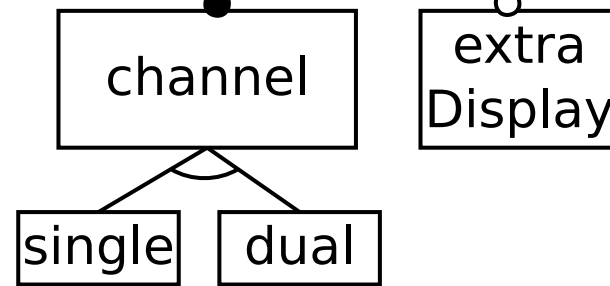


Hybrid Models



Features

telematics
System



Calibrations

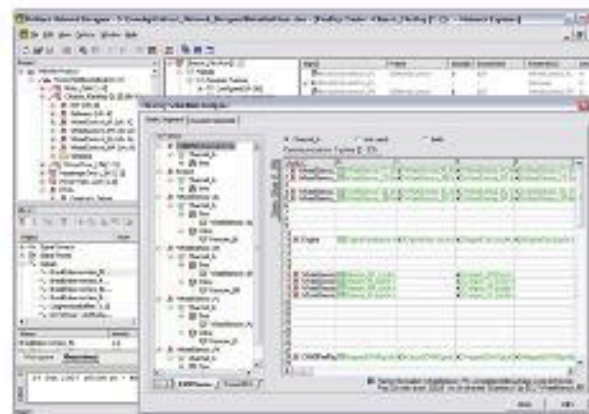


OS Generation

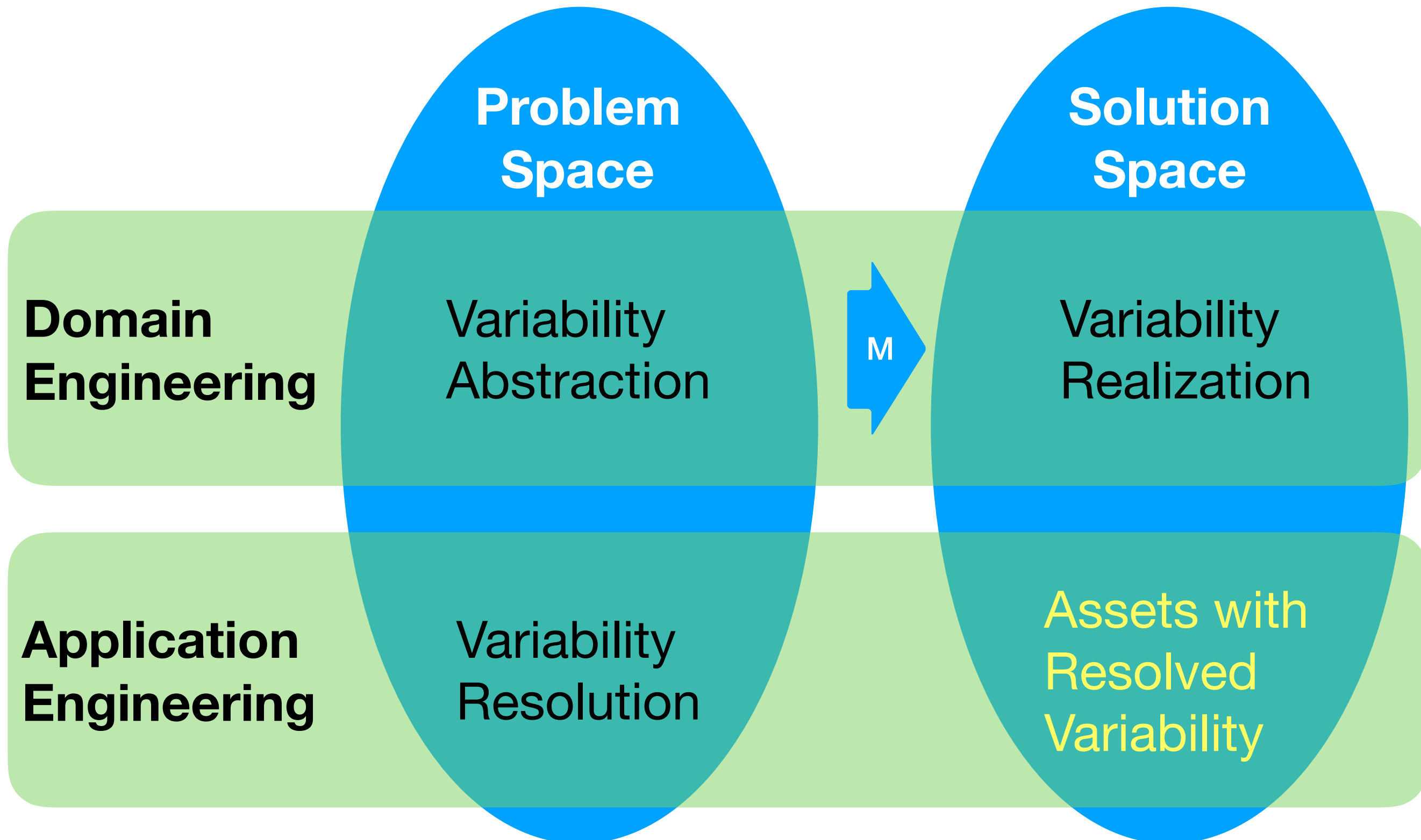


Device	OS	Support	Support	Support	Support	Support	Support
Device	OS	Support	Support	Support	Support	Support	Support
StarDiagnose	WinCE	Support	Support	Support	Support	Support	Support
StarDiagnose	WinCE	Support	Support	Support	Support	Support	Support
StarDiagnose	WinCE	Support	Support	Support	Support	Support	Support
StarDiagnose	WinCE	Support	Support	Support	Support	Support	Support
StarDiagnose	WinCE	Support	Support	Support	Support	Support	Support
StarDiagnose	WinCE	Support	Support	Support	Support	Support	Support
StarDiagnose	WinCE	Support	Support	Support	Support	Support	Support
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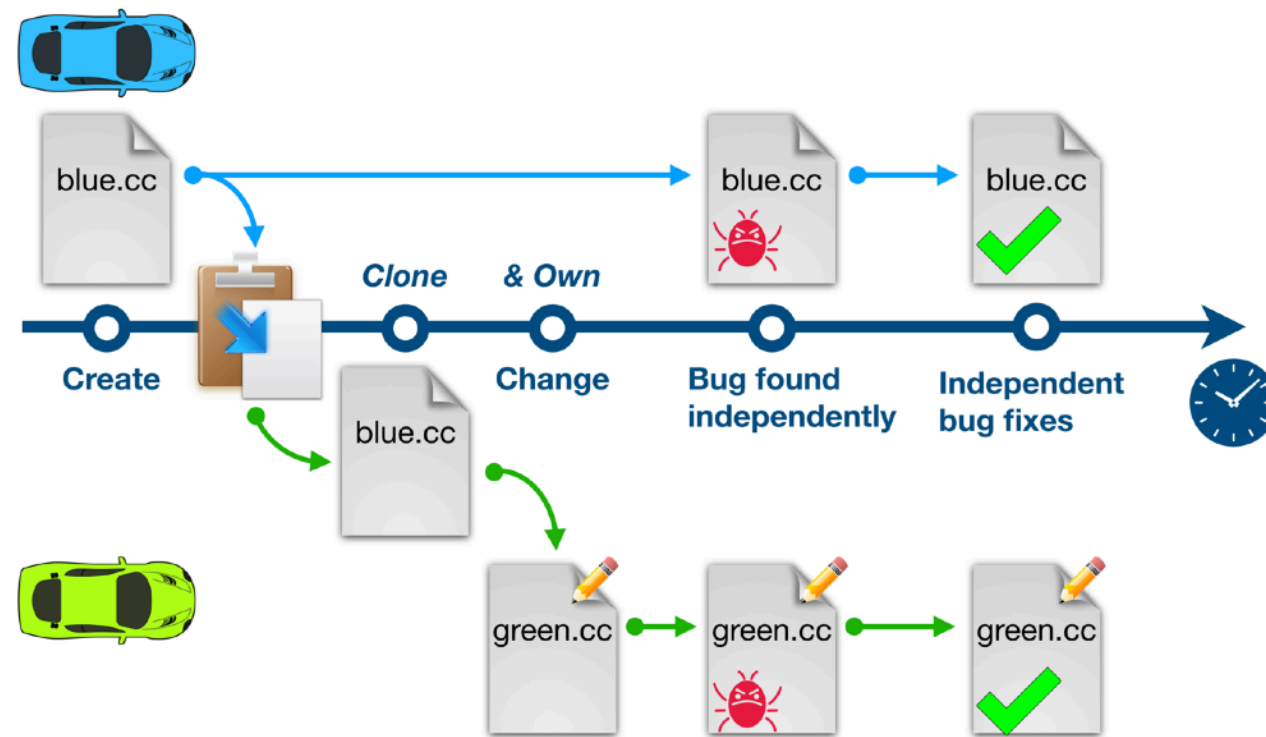
HW/SW Mapping



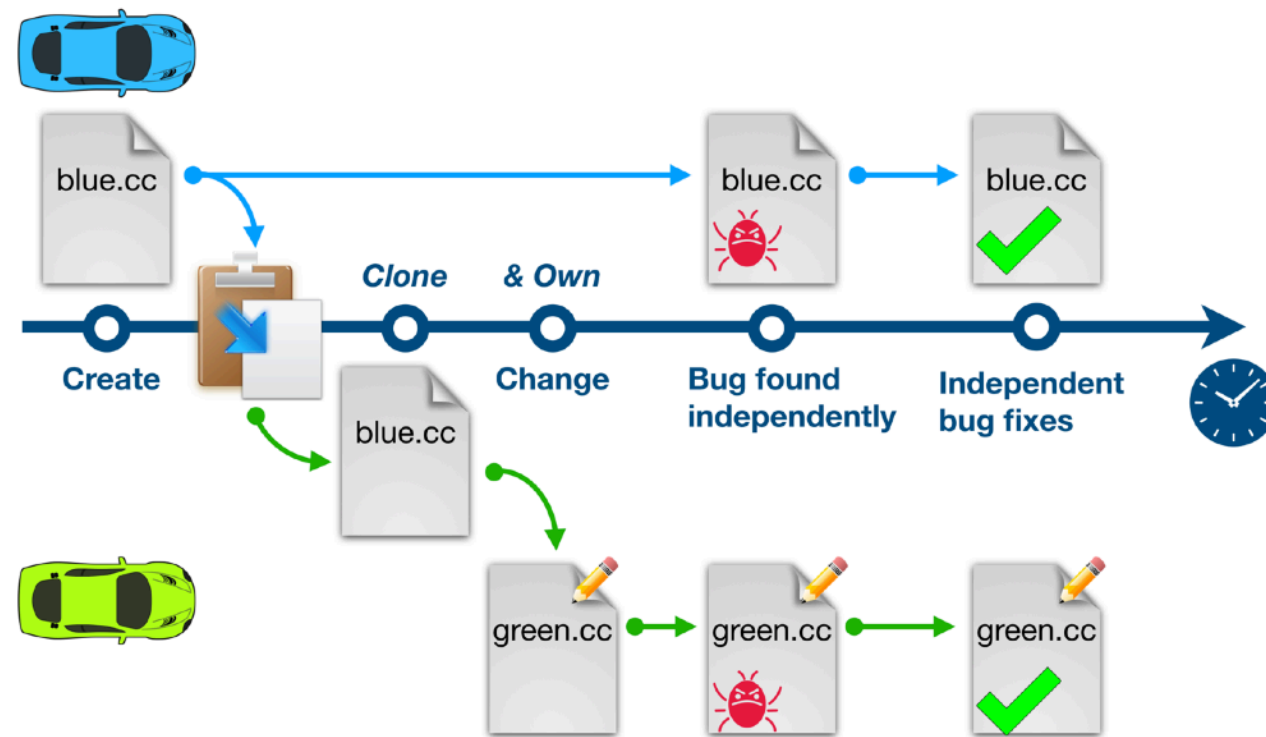
Software Product Line



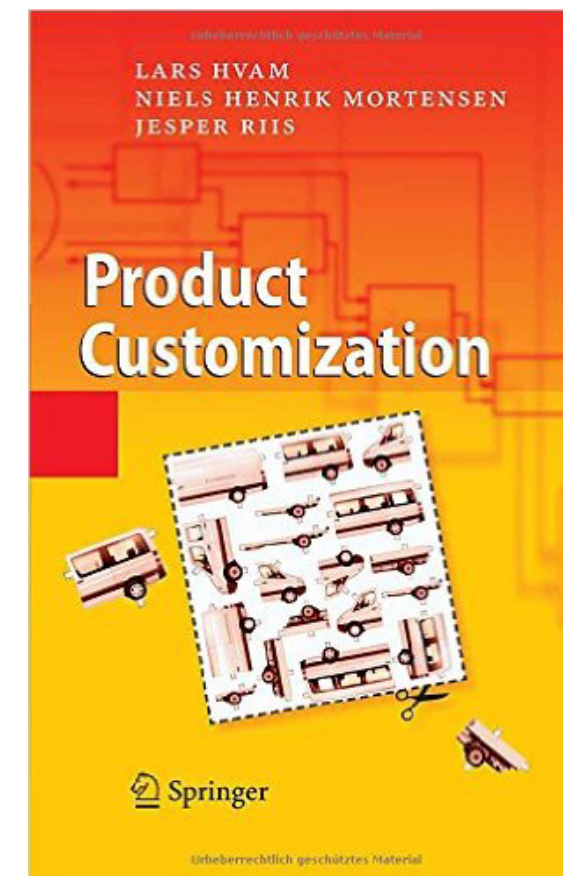
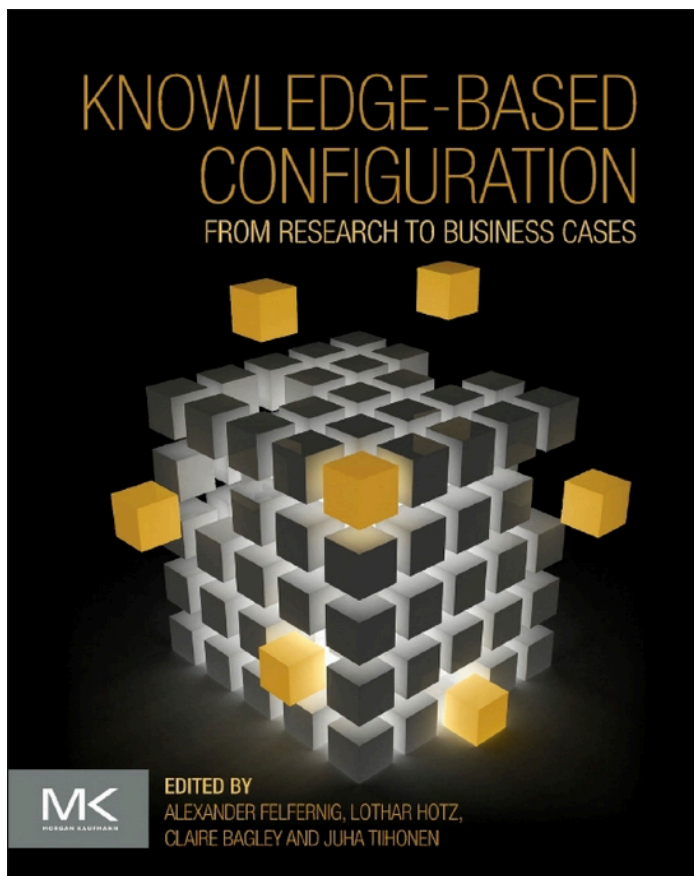
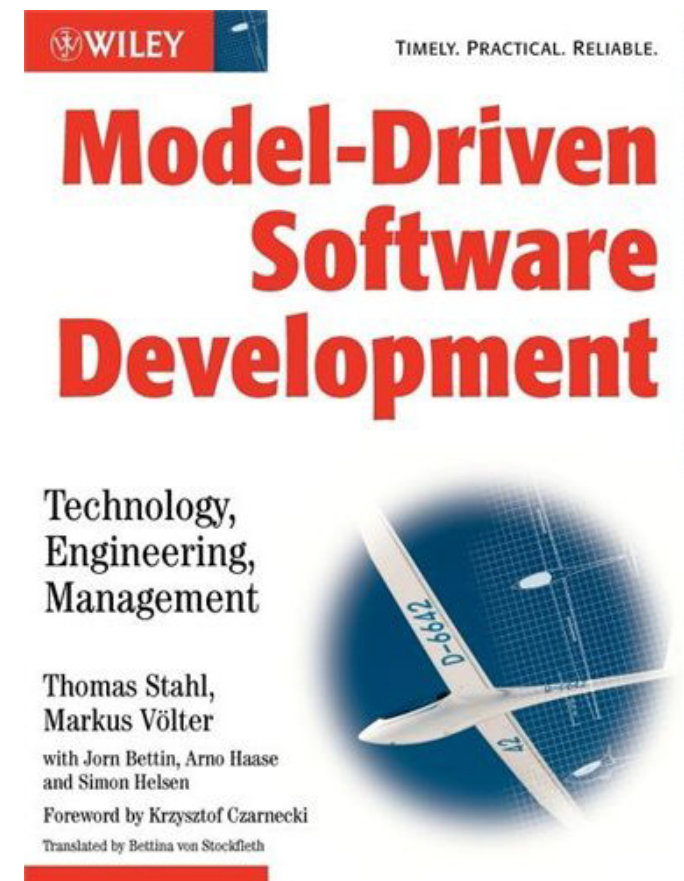
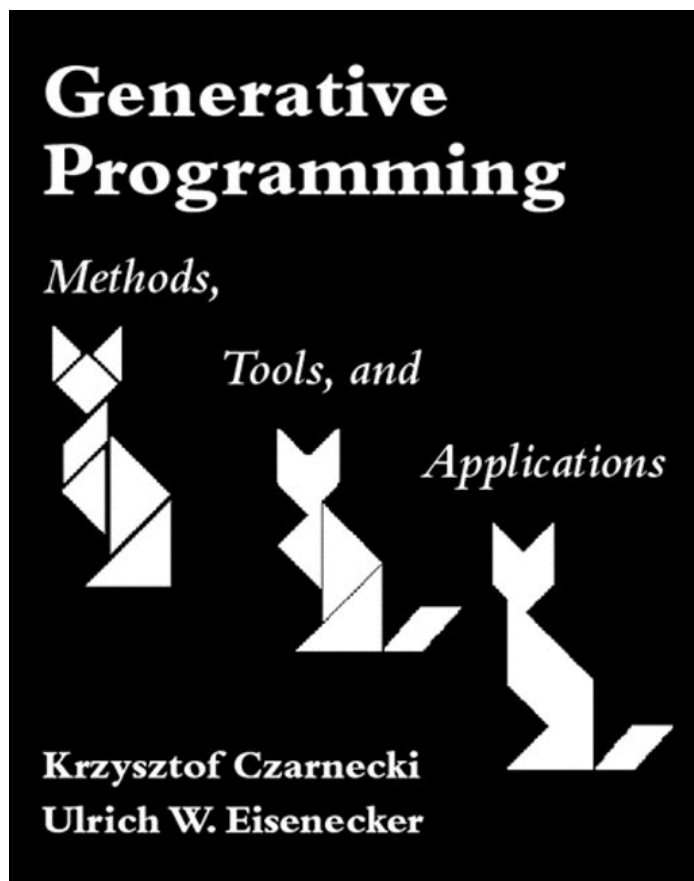
Is Clone and Own Always Bad?



Is Clone and Own Always Bad?



not if the cost of cloning is less
than the cost of an SPL 🤔



Exercise

- Example Domain: *Traffic Lights*
 - Feature-oriented commonality/variability analysis
 - Domain concept analysis
 - Application configuration
- Apply *Example-Driven Modeling*
- Use Clafer & Web Tools
 - Tutorial style
 - Hands-on
 - Small exercises

Interactive Tutorial

<http://t3-necsis.cs.uwaterloo.ca:8098/>

Use Chrome or Firefox

Indent code with spaces, not tabs